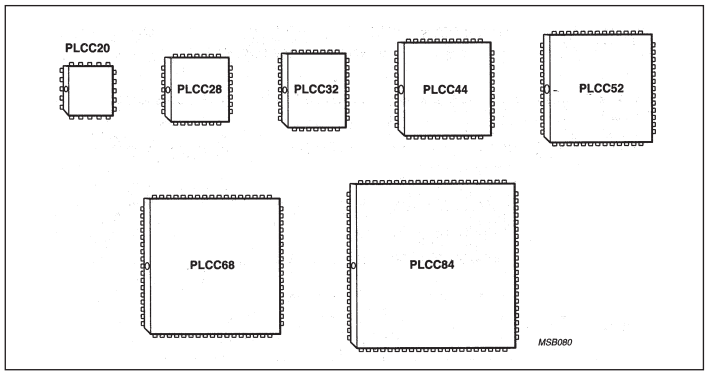
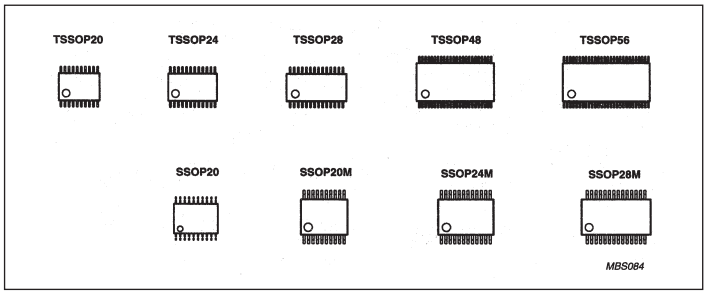
BOÎTIERS CIRCUITS-INTÉGRÉS

PLASTIC LEADED CHIP CARRIER (PLCC)



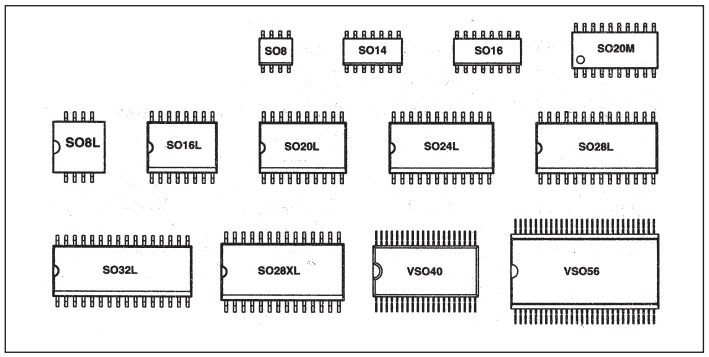
Boîtier	Dimensions (l x L) mm	Pas mm/mils	Boîtier	Dimensions (l x L) mm	Pas mm/mils
PLCC20	8.92 x 8.92	1.27/50	PLCC52	19.08 x 19.08	1.27/50
PLCC28	11.46 x 11.46	1.27/50	PLCC68	24.16 x 24.16	1.27/50
PLCC32	13.97 x 11.43	1.27/50	PLCC84	29.24 x 29.24	1.27/50
PLCC44	16.54 x 16.54	1.27/50			

SHRINK SMALL OUTLINE (SSOP) THIN SMALL OUTLINE (TSSOP)



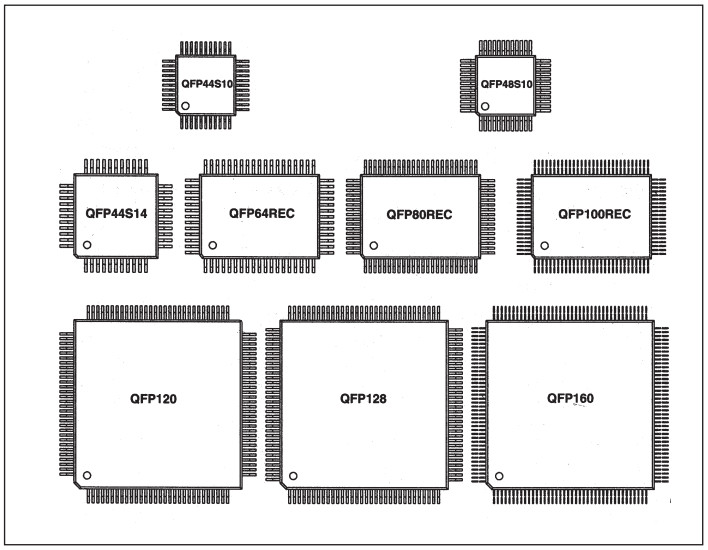
Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils	Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils
SSOP20	4.4 x 6.5 x 1.3	0.65	TSSOP20	4.4 x 6.5 x 0.85	0.65
SSOP20M	5.3 x 7.2 x 1.7	0.65	TSSOP24	4.4 x 7.8 x 0.85	0.65
SSOP24M	5.3 x 8.2 x 1.7	0.65	TSSOP28	4.4 x 9.7 x 0.85	0.65
SSOP28M	5.3 x 10.2 x 1.7	0.65	TSSOP48	6.1 x 12.5 x 1.0	0.5
			TSSOP56	6.1 x 14.0 x 1.0	0.5

SMALL OUTLINE (SO) VERY SMALL OUTLINE (VSO)

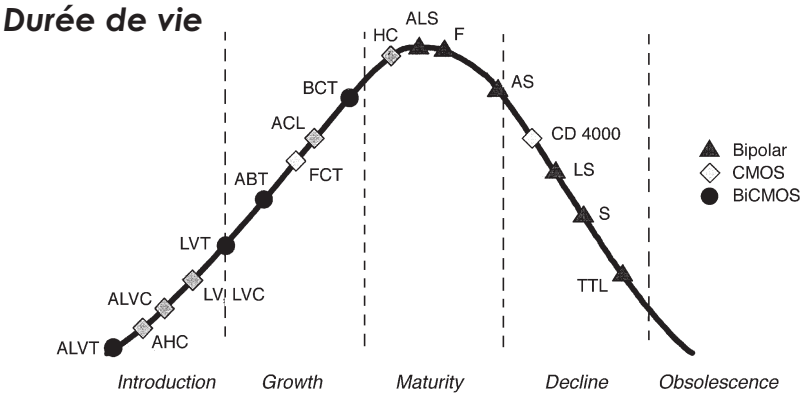


Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils	Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils
SO8	3.9 x 4.9 x 1.35	1.27/50	SO24	7.5 x 15.4 x 2.35	1.27/50
SO8L	7.5 x 7.6 x 2.35	1.27/50	SO28L	7.5 x 17.9 x 2.35	1.27/50
SO14	3.9 x 8.6 x 1.35	1.27/50	SO28XL	8.4 x 18.0 x 2.70	1.27/50
SO16	3.9 x 9.9 x 1.35	1.27/50	SO32L	7.5 x 20.5 x 2.35	1.27/50
SO16L	7.5 x 10.3 x 2.85	1.27/50	VSO40	7.5 x 15.6 x 2.35	0.762/30
SO20L	7.5 x 12.8 x 2.35	1.27/50	VSO56	11.1 x 22.0 x 2.9	0.75/ -
SO20M	5.3 x 12.6	1.27/50			

QUAD FLAT-PACK (QFP)



Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils	Boîtier	Dimensions (l x L x Ép) mm	Pas mm/mils
QFP32	7 x 7 x 1.4	0.8	QFP80REC	14 x 20 x 2.75	0.8
QFP44S10	10 x 10 x 1.75	0.8	QFP100REC	14 x 20 x 2.75	0.65
QFP44S14	14 x 14 x 2.2	1.0	QFP120	28 x 28 x 3.35	0.8
QFP48S10	10 x 10 x 1.75	0.75	QFP128	28 x 28 x 3.35	0.8
QFP52	10 x 10 x 2.0	0.65	QFP160	28 x 28 x 3.35	0.65
QFP64REC	14 x 20 x 2.75	1.0			



Applications logiques générales

	S-L-O-W		> 4 MHz		> 8 MHz		> 16 MHz		> 33 MHz		> 50 MHz		>75 MHz
	5 V	3 V	5 V	3 V	5 V	3 V	5 V	3 V	5 V	3 V	5 V	3 V	3 V
General Bus Interface	TTL	LV HC	LS TTL S	LV HC	ALS AS F	LV LVC AC	F BCT ACT AHC	LVC AC	ABT	LVT	ABT ABTWB+	LVT ALVC FB	ALVC GTL FB
Gate Functions	TTL	LV HC	LS TTL S	LV HC	ALS AS F	LV LVC AC	F BCT ACT AHC	LVC AC					

Comparaisons logiques des familles

Family	Year Introduced	Technomogy	Power	Drive (-IOH/IOL)	Speed (ns)	Volp Noise
TTL	1968	Bipolar	High	-15/24	18	< 0.8 V
S	1974	Bipolar	High	-15/64	9	< 0.8 V
LS	1976	Bipolar	Médium	-15/24	18	< 0.8 V
ALS	1979	Bipolar	Medium	-15/24	10	< 0.8 V
HC/HCT	1975	CMOS	-Low	-8/18	25	< 1 V
F	1983	Bipolar	+ High	-15/64	6.5	< 0.8 V
AS	1982	Bipolar	+ High	-15/64	6.2	< 0.8 V
FCT	1986	CMOS	Low	-32/64	6.5/4.8	> 2 V
BCT	1987	BiCMOS	+ Low	-15/64	5.5	< 0.8 V
AC/ACT	1985	CMOS	Low	-24/24	10	> 2 V
ABT	1990	BiCMOS	Low	-32/64	4.1	< 0.8 V
FCT-T	1991	CMOS	Low	-32/64	6.5/4.8/4.1	< 1 V
LVT	1992	BiCMOS	- Low	-32/64	4.2	< 0.8 V
LVC/ALVC	1993	CMOS	- Low	-24/24	7/3.6	< 0.8 V
ETL/ABTE	1993	BiCMOS	Low	-60/90	4.6	< 0.8 V
CBT	1994	BiCMOS	Low	0	250 ps	< 0.8 V
AHC/AHCT	1996	CMOS	- Low	-8/8	8.5	< 1 V

PORTES LOGIQUES

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
ET									
Quatre portes ET à 2 entrées	08		•	•	•	•	•	•	•
Trois portes ET à 3 entrées	11		•		•		•		
Deux portes ET à 4 entrées	21		•						
OU									
Quatre portes OU à 2 entrées	32	•	•	•	•	•	•	•	
Quatre portes OU exclusifs à 2 entrées	86		•	•	•	•	•		
Quatre portes OU exclusifs à 2 entrées, C.O.	136		•						
ET-NON									
Quatre portes ET-NON à 2 entrées	00	•	•	•	•	•	•	•	•
Quatre portes ET-NON à 2 entrées, C.O.	03	•	•		•				
Trois portes ET-NON à 3 entrées	10	•	•	•	•	•	•	•	
Deux portes ET-NON à 4 entrées	20	•	•		•		•		
Deux portes ET-NON à 4 entrées, C.O.	22		•						
Une porte ET-NON à 8 entrées	30		•						
4 portes amplificatrices ET-NON à 2 entrées	37	•	•						
4 portes amplificatrices ET-NON à 2 entrées, C.O.	38	•	•			•			
OU-NON									
Quatre portes OU-NON à 2 entrées	02		•	•	•	•	•		
Trois portes OU-NON à 3 entrées	27		•	•					
Deux portes OU-NON à 4 entrées	4002				•				
INVERSEURS									
Six triggers de Schmitt, inverseurs	14		•	•	•		•		
4 portes ET-NON trigger de Schmitt à 2 entrées	132		•	•	•				
AMPLIFICATEURS									
Six inverseurs	04	•	•	•	•	•	•	•	
Six inverseurs, C.O.	05	•	•		•				
Six inverseurs-amplificateurs, C.O., 30 V	06	•	•			•			
Six amplificateurs, C.O., 30 V	07	•	•			•			
Quatre amplificateurs 3 états	125		•	•	•		•	•	
Quatre amplificateurs 3 états	126	•			•				
Double et quadruple amplif. 3 états	367		•		•				
Double et quadruple amplif.-inverseurs 3 états	368			•	•			•	

AMPLIFICATEURS DE BUS

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
Octuple amplificateur de ligne invers., 3 états	240		•	•	•	•	•	•	
Octuple amplificateur de ligne, 3 états	241		•	•	•	•	•	•	
2 quadruples amplificateurs de ligne, 3 états	244		•	•	•	•	•	•	•
Octuple amplif. de ligne, bidirectionnel, 3 états	245		•	•	•	•	•	•	•
Octuple amplif.-inverseur de ligne, 3 états	540						•		
Octuple amplificateur de ligne, 3 états	541			•	•	•			
Oct. émetteur-récepteur invers. de bus, 3 états	623					•			
Oct. émetteur-récepteur invers. de bus, 3 états	640			•					
Oct. émetteur-récept.r de bus + registre, 3 états	646						•		
Octuple émetteur-récepteur inverseur de bus avec registre, 3 états.	648						•		
Six amplif.-inverseurs adaptateurs de niveaux	4049				•				
Six amplificateurs adaptateurs de niveaux	4050				•				

BASCULES

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
BASCULES D									
Deux bascules type D, 1R, 1S	74	•	•	•	•	•	•	•	•
Deux fois 2 verrous	75	•	•		•				
Quadruple bascule D 4 bits, 3 états	173		•		•				
Sextuple bascule type D	174		•	•	•	•	•	•	
4 bascule type D, sorties complémentaires	175	•	•	•	•	•	•	•	
Octuple bascule D, 3 états	374		•	•	•		•	•	
Octuple bascule D, avec validation	377				•		•	•	
Sextuple bascule D, avec validation	378						•		
Oct. bascule type D, sorties complém. 3 états	534							•	•
Octuple verrou type D, 3 états	573			•	•	•		•	
Octuple bascule type D, 3 états	574			•	•	•	•	•	
BASCULES JK									
Deux bascules JK, 1K, 1R, 1J	73		•						
2 bascules JK à déclenchement sur front montant	109	•					•	•	
MULTIVIBRATEURS MONOSTABLES									
2 multivibrateurs monostables redéclenchables	123		•	•	•				
2 multivibr. monostables avec triggers de Schmitt	221	•	•						
2 multivibrateurs	4538				•				

BASCULES (suite)

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
VERROUS									
Un verrou adressable 8 bits, validation et RAZ	259	•		•	•				
Octuple bascule type D, RAZ	273		•	•	•	•	•		
Quatre bascules RS	279	•	•						
Octuple verrou, 3 états	373			•	•	•	•	•	
Quadruple multiplexeur 2 vers 1 avec mémoire	399					•		•	
Oct. verrou type D, sorties complém.s 3 états	563							•	
Octuple bascule type D, sorties complém. 3 états	564							•	
FONCTIONS ARITHMÉTIQUES									
Comparateur 4 bits	85	•	•	•	•	•			
Un additionneur binaire 4 bits	283	•				•			
Comparateur 8 bits, sortie complémentée	520						•		
Comparateur 8 bits, sortie complémentée	521						•		•
Compar. d'égalité 8 bits, validation de la sortie	688			•	•				
COMPTEURS									
Une décade de comptage sortie 4 bits	90		•						
Un compteur-diviseur par 2, 6, 12	92	•	•						
Un compteur binaire 4 bits	93		•						
Un compteur binaire 4 bits,reset asynchrone programmable	161	•	•	•	•	•	•	•	•
Un compteur binaire 4 bits programmable RAZ synchrone	163				•	•	•	•	
1 compteur-décompteur binaire 4 bits progrble	169					•	•	•	
1 compteur-décompteur binaire 4 bits	191	•	•		•		•	•	
1 compteur-décompteur BCD 4 bits	192				•				
1 compteur-décompteur binaire 4 bits	193	•	•	•	•				
Deux compteurs binaires 4 bits	393	•	•	•	•	•			
Un compteur binaire 14 bits	4020				•				
Un compteur binaire 12 bits	4040			•	•				
Un compteur-diviseur binaire 14 étages avec oscillateur	4060				•				

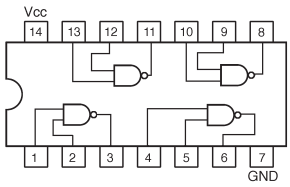
REGISTRES À DÉCALAGE

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
Un registre à décalage 8 bits, entrées séries sorties parallèles	164		•		•				
Un registre à décalage 8 bits entrées parallèles, sortie série	165				•				
Un registre à décalage 8 bits, entrées séries ou parallèles, RAZ	166		•	•					
Un registre à décalage universel 8 bits, 3 états	299					•	•	•	
Un registre à décalage universel 8 bits	323							•	
Un registre à décalage 8 bits avec registre de sortie, 3 états	595				•				
Un registre à décalage à 8 étages, sortie parallèle, verrou 3 états	4094				•				

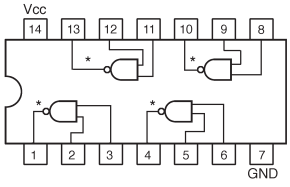
DÉCODEURS, MULTIPLEXEURS, DÉMULTIPLEXEURS

	Type	STD	LS	HCT	HC	FAST	AC	ACT	F
			FAMILLE LOGIQUE						
Décodeur BCD-décimal	42	•	•						
Décodeur BCD-7 segments, C.O. (15 V)	47	•	•						
1 décodeur-démultiplexeur 3 vers 8 entrées mémorisées	137		•		•				
1 décodeur-démultiplexeur 3 vers 8	138		•	•	•	•	•	•	
Deux décodeurs-démultiplexeurs 2 vers 4	139		•	•	•	•	•	•	
1 décodeur BCD-décimal, C.O. (15 V)	145		•						
1 codeur de priorité 8 vers 3	148		•		•				
1 multiplexeur 8 vers 1, sorties complém.	151	•	•		•		•	•	
Double multiplexeur 4 vers 1	153	•	•				•	•	
Double décodeur-démultiplexeur 2 vers 4, C.O.	156		•						
Quadruple multiplexeur 2 vers 1	157	•	•	•	•		•	•	
Quadruple multiplexeur 2 vers 1 sorties complémentées	158					•	•	•	
Un décodeur-démultiplexeur 3 vers 8 avec adresses mémorisées	237				•				
Un décodeur-démultiplexeur 3 vers 8	238				•				
Un multiplexeur 8 vers 1, 3 états	251				•		•	•	
Un double multiplexeur 4 vers 1, 3 états	253						•	•	
Un quadruple multiplexeur 2 vers 1, 3 états	257			•	•	•	•	•	
Un quadruple multiplexeur 2 vers 1 sortie complémentée, 3 états	258						•	•	
Un générateur-contrôleur de parité 9 bits	280					•	•		
1 multiplexeur-démultiplex. analogique 8 voies	4051				•				
Double multiplexeur-démultiplex. analog. 4 voies	4052				•				
Triple multiplexeur-démultiplex. analog. 2 voies	4053				•				

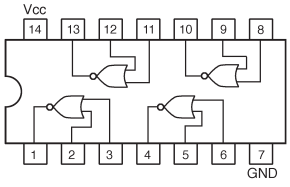
74 00 *4 portes ET-NON à 2 entrées*



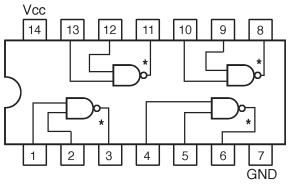
74 01 *4 portes ET-NON à 2 entrées C.O.*



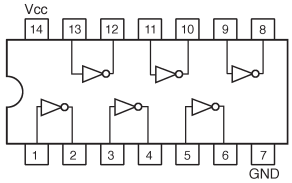
74 02 *4 portes OU-NON à 2 entrées*



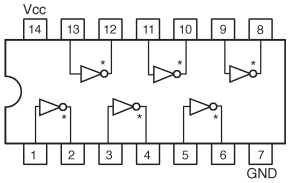
74 03 *4 portes ET-NON à 2 entrées C.O.*



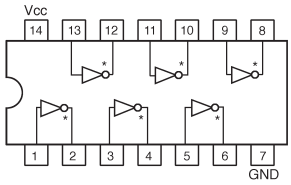
74 04 *6 inverseurs*



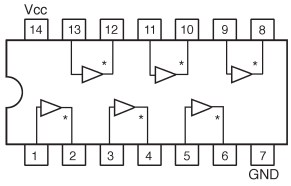
74 05 *6 inverseurs C.O.*



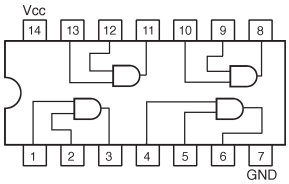
74 06 *6 inverseurs C.O. 30 V*



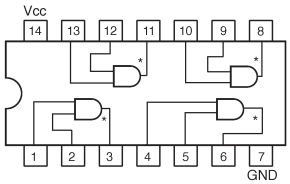
74 07 *6 amplificateurs C.O. 30 V*



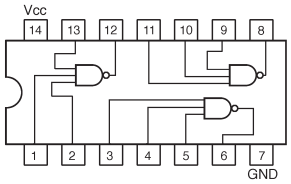
74 08 *4 portes ET à 2 entrées*



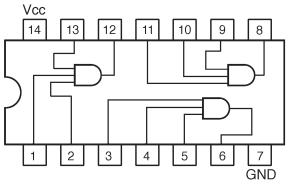
74 09 *4 portes et à 2 entrées C.O.*



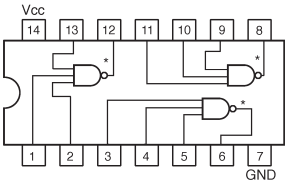
74 10 *3 portes ET-NON à 3 entrées*



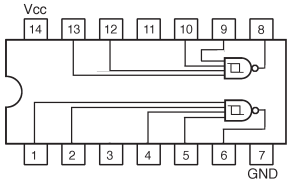
74 11 *3 portes ET à 3 entrées*



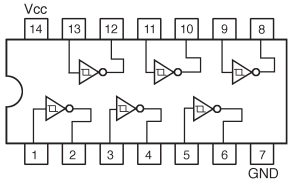
74 12 *3 portes ET-NON à 3 entrées C.O.*



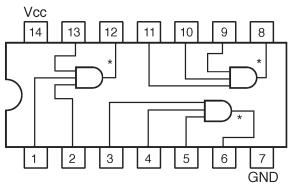
74 13 *2 portes ET-NON à 4 entrées avec bascule de schmitt*



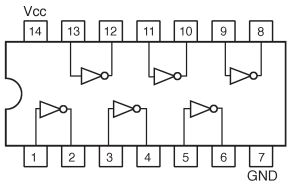
74 14 *6 inverseurs avec bascule de schmitt*



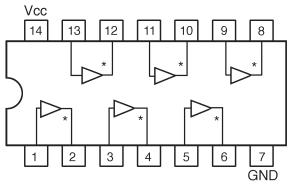
74 15 *3 portes ET à 3 entrées C.O.*



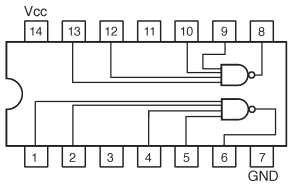
74 16 *6 inverseurs C.O.*



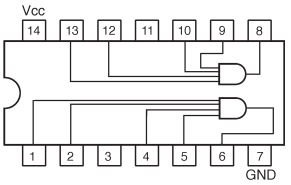
74 17 *6 amplificateurs C.O. 15 V*



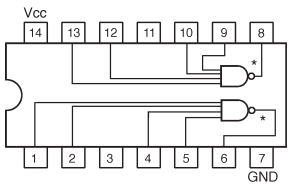
74 20 *2 portes ET-NON à 4 entrées*



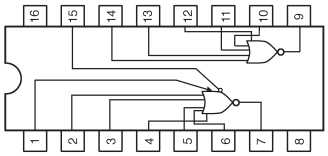
74 21 *2 portes ET à 4 entrées*



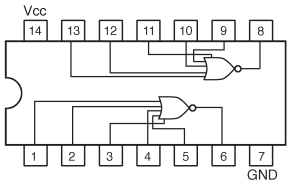
74 22 *2 portes ET-NON à 4 entrées C.O.*



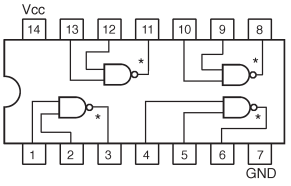
74 23 *2 portes OU-NON à 4 entrées avec échantillonnage et 1 expansible*



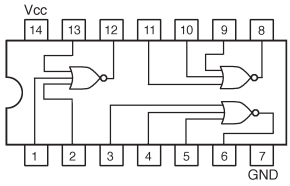
74 25 *2 portes OU-NON à 4 entrées et échantillonnage*



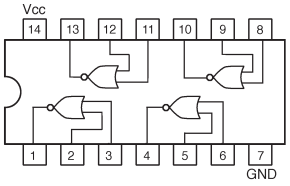
74 26 *4 portes ET-NON à 2 entrées C.O.*



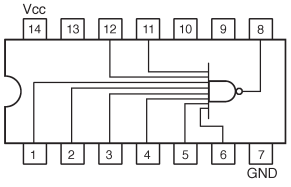
74 27 *3 portes OU-NON à 3 entrées*



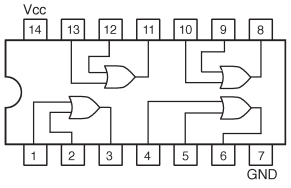
74 28 *4 portes OU-NON à 2 entrées*



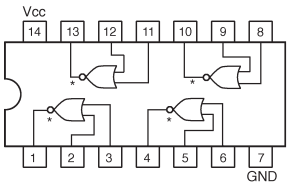
74 30 *1 porte ET-NON à 8 entrées*



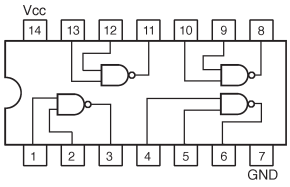
74 32 *4 portes OU à 2 entrées*



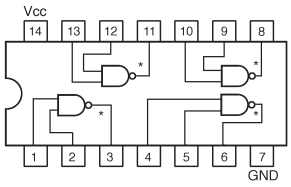
74 33 *4 portes OU-NON à 2 entrées C.O.*



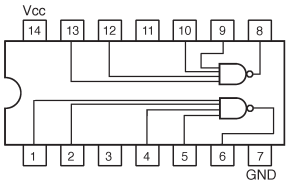
74 37 *4 portes amplificatrices ET-NON à 2 entrées*



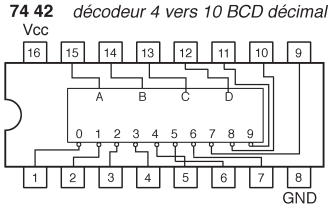
74 38 *4 portes amplificatrices ET-NON à 2 entrées C.O.*



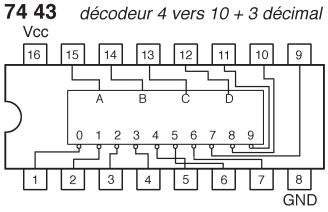
74 40 *2 portes ET-NON à 4 entrées*



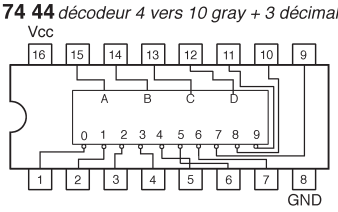
74 42 *décodeur 4 vers 10 BCD décimal*



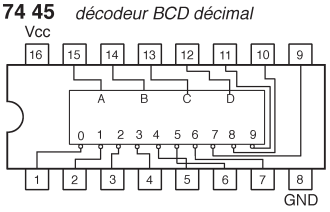
74 43 *décodeur 4 vers 10 + 3 décimal*



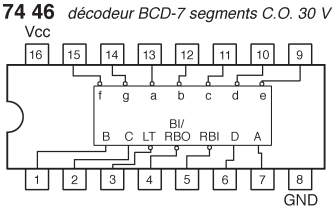
74 44 *décodeur 4 vers 10 gray + 3 décimal*



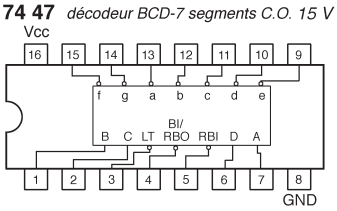
74 45 *décodeur BCD décimal*



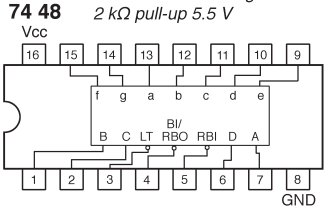
74 46 *décodeur BCD-7 segments C.O. 30 V*



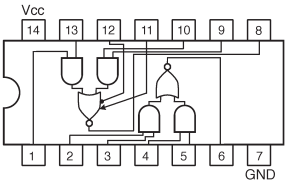
74 47 *décodeur BCD-7 segments C.O. 15 V*



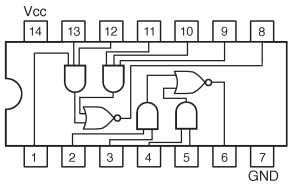
74 48 *décodeur BCD-7 segments 2 kΩ pull-up 5.5 V*



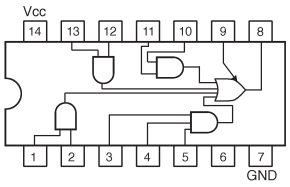
74 50 *2 portes OU-NON avec 2 ET à 2 entrées dont 1 expansible*



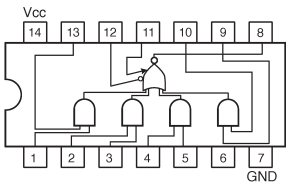
74 51 *2 portes ET-OU-NON à 2 x 2 entrées*



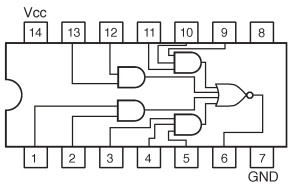
74 52 *Expandable 2 2 2 3 inp. AND OR*



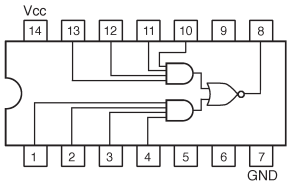
74 53 *1 porte OU-NON avec 4 ET à 2 entrées expansible*



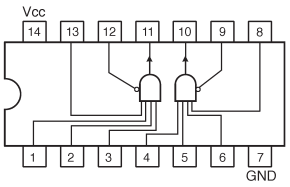
74 54 *4 wide AND OR INV*



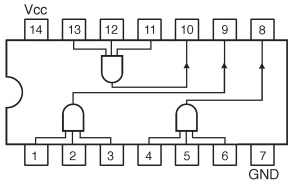
74 55 *1 porte OU-NON avec 2 ET à 4 entrées*



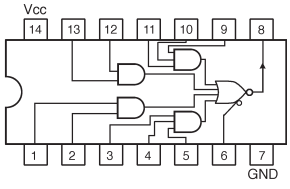
74 60 *2 expanseurs à 4 entrées*



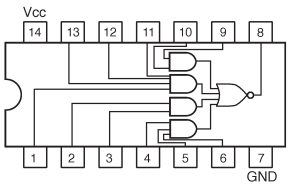
74 61 *triple 3 input expander*



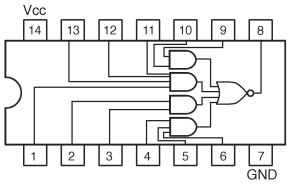
74 62 *3 2 2 3 input AND OR*



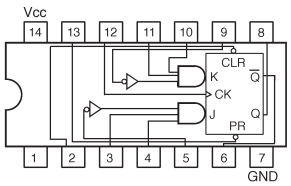
74 64 *4 2 3 2 input AND OR INV*



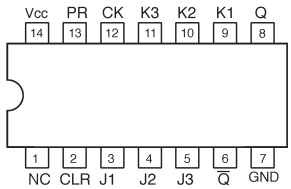
74 65 *4 2 3 2 input AND OR INV*



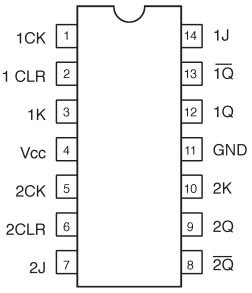
74 70 *1 bascule JK à 3 entrées avec R et S*



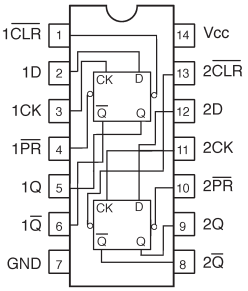
74 72 *1 bascule JK maître-esclave à 3 entrées avec R et S*



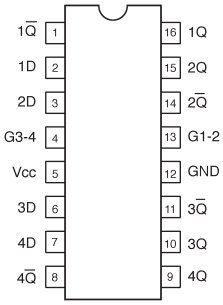
74 73 *2 bascules JK avec RAZ*



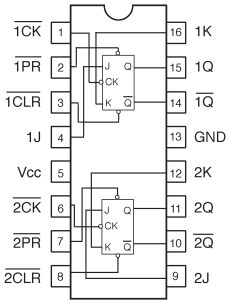
74 74 *2 bascules D avec R et S*



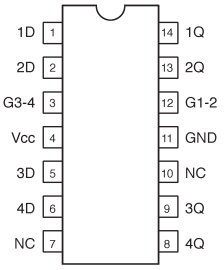
74 75 *2 doubles bascules à verrouillage*



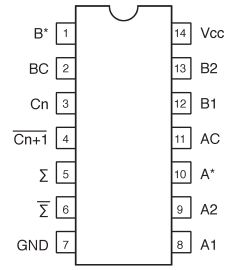
74 76 *2 bascules JK avec R et S*



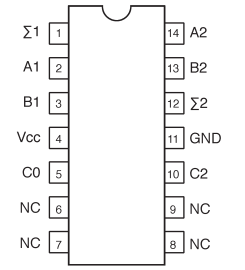
74 77 *verrou 4 bits*



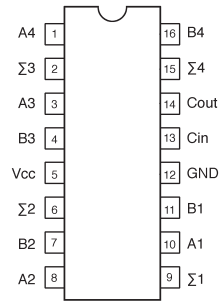
74 80 *additionneur complet 1 bit*



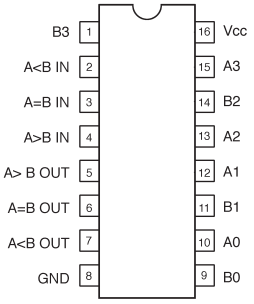
74 82 *additionneur complet 2 bits*



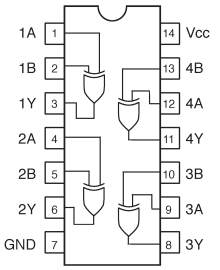
74 83 *additionneur complet 4 bits*



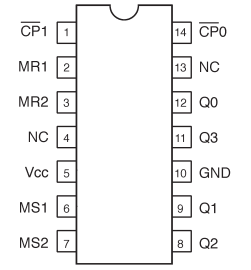
74 85 *comparateur 4 bits*



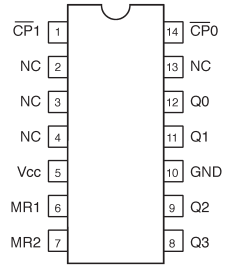
74 86 *4 portes OU-EXCLUSIF à 2 entrées*



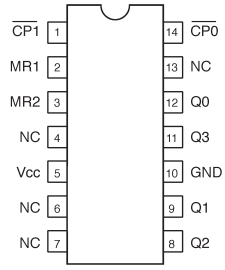
74 90 *compteur décimal*



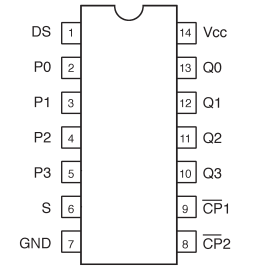
74 92 *compteur hexadécimal*



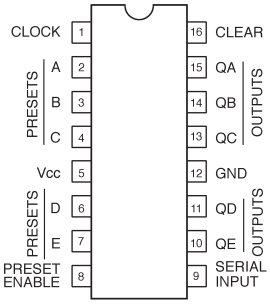
74 93 *compteur binaire asynchrone 4 bits*



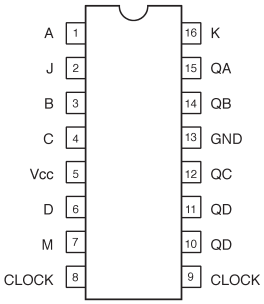
74 95 *registre à décalage 4 bits*



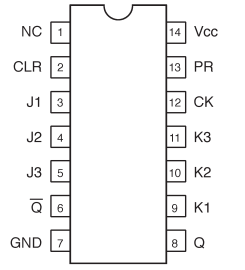
74 96 *registre à décalage 5 bits*



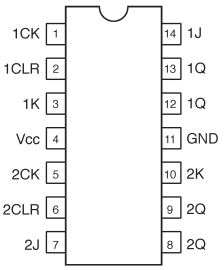
74 99 *4 bit R shift L shift register*



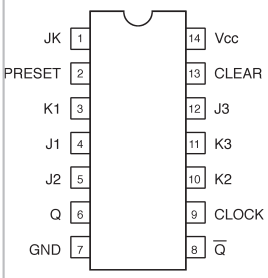
74 102 *1 bascule JK maître-esclave à 3 entrées avec R et S*



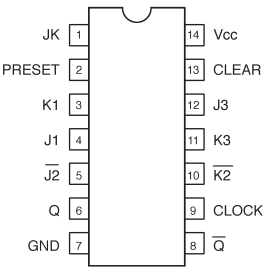
74 103 *JK flip flop*



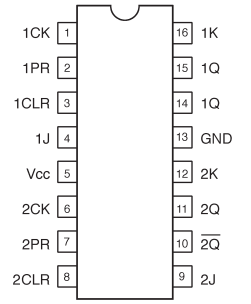
74 104 *1 bascule JK maître-esclave à 3 entrées av ec R et S*



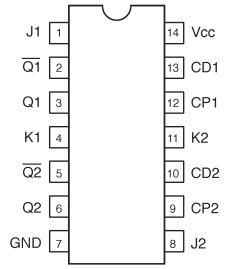
74 105 *1 bascule JK maître-esclave à 3 entrées av ec R et S*



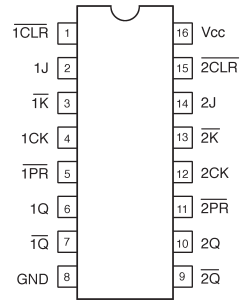
74 106 *dual JK flip flop*



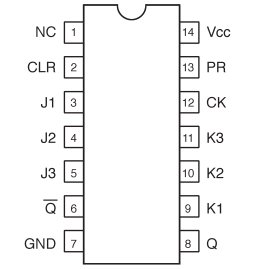
74 107 *2 bascules JK avec RAZ*



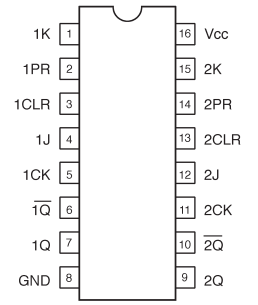
74 109 *2 bascules JK à déclenchemen sur front montant*



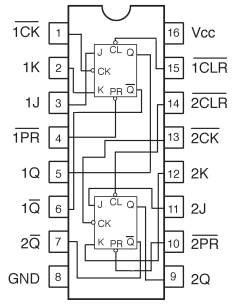
74 110 *dual JK flip flop with data lockout*



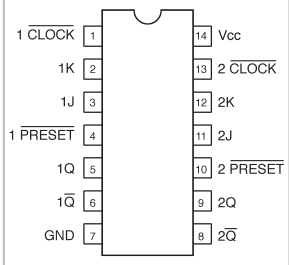
74 111 *2 bascules JK avec R et S*



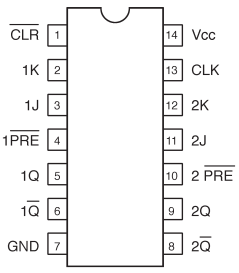
74 112 *2 bascules JK avec R et S*



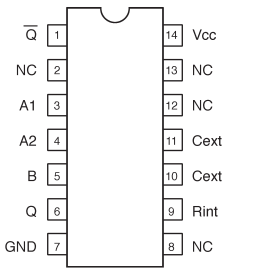
74 113 *2 bascules JK avec S déclenchement sur front descendant*



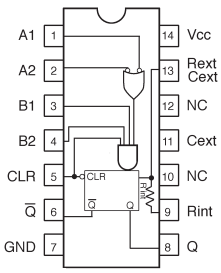
74 114 *dual JK flip flop*



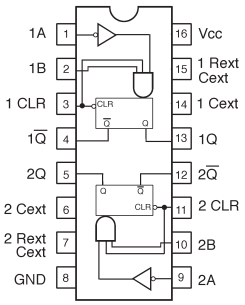
74 121 *monostable multivibrateur avec bascule de schmitt en entrées*



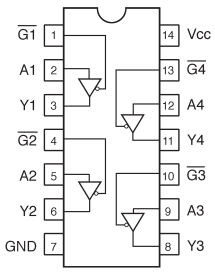
74 122 *monostable redéclenchable avec RAZ*



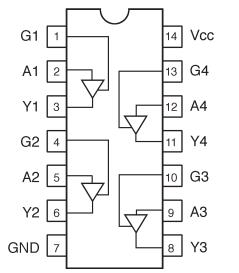
74 123 *2 multivibrateurs monostables redéclenchables*



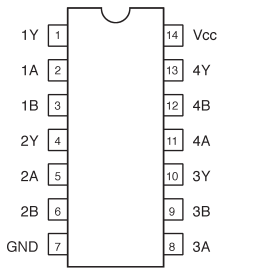
74 125 *4 amplificateurs 3 états*



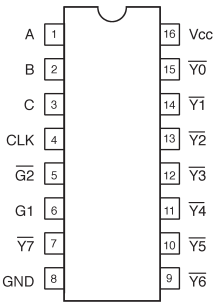
74 126 *4 amplificateurs 3 états*



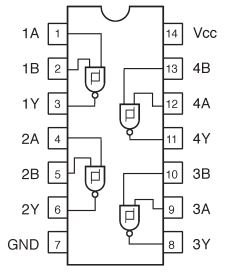
74 128 *4 portes OU-NON à 2 entrées*



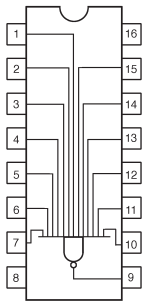
74 131 *3 line to 8 line decoders/demultiplexers with address registers*



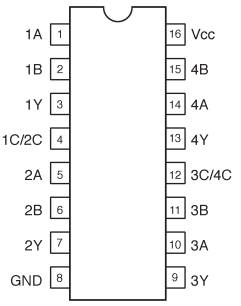
74 132 *4 portes ET-NON trigger de Schmitt à 2 entrées*



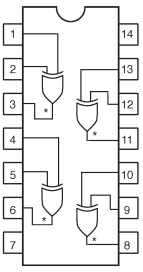
74 133 *1 porte ET-NON à 13 entrées*



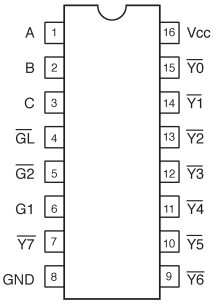
74 135 *quad exclusive OR NOR*



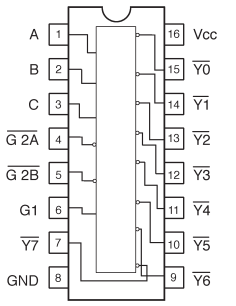
74 136 *4 portes OU exclusifs à 2 entrées, C.O.*



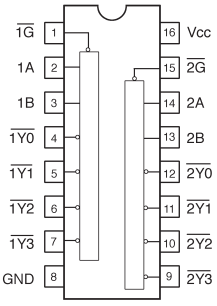
74 137 *1 décodeur-démultiplexeur 3 vers 8 entrées mémorisées*



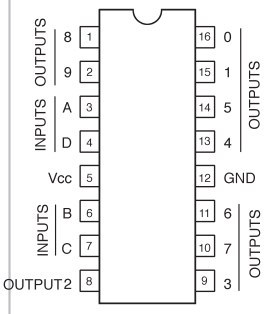
74 138 *1 décodeur-démultiplexeur 3 vers 8*



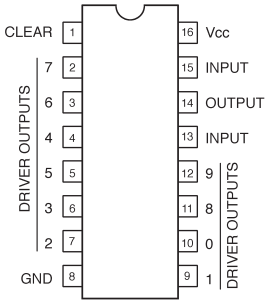
74 139 *2 décodeurs-démultiplexeurs 2 vers 4*



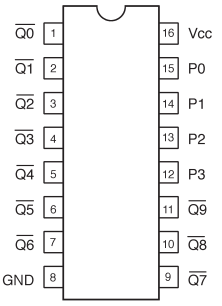
74 141 *décodeur BCD décimal*



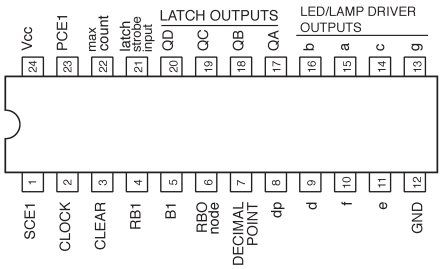
74 142 *BCD counter/latch/ decoder/driver*



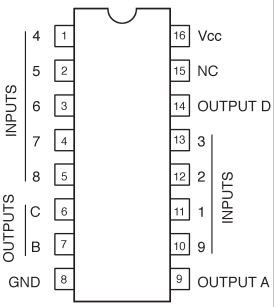
74 145 *1 décodeur BCD-décimal C.O. (15 V)*



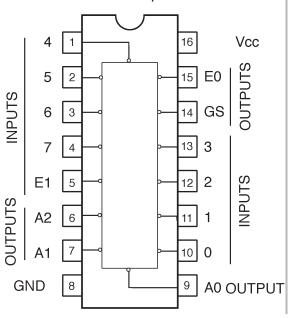
74 143/74 144 *4 bit counter/latch/7 segment lamp driver*



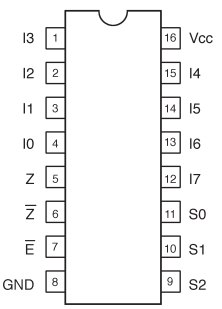
74 147 *10 to 4 line priority encoder*



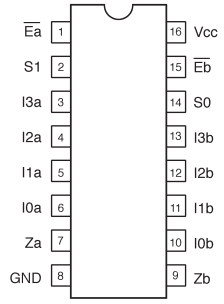
74 148 *1 codeur de priorité 8 vers 3*



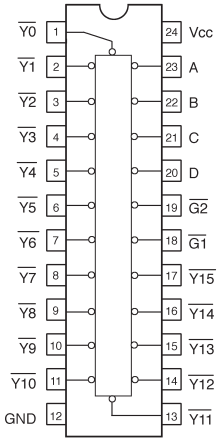
74 151 *1 multiplexeur 8 vers 1 sorties complémentaires*



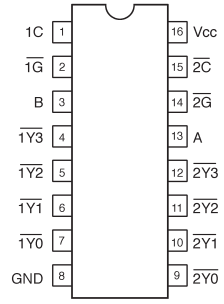
74 153 Double multiplexeur 4 vers 1



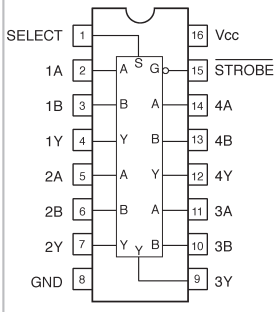
74 154 *décodeur-démultiplexeur 4 vers 16*



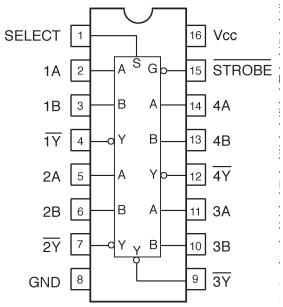
74 155 *double décodeur-démultiplexeur 2 vers 4*



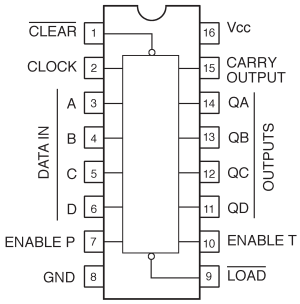
74 157 *quadruple sélecteur-multiplexeur 2 vers 1*



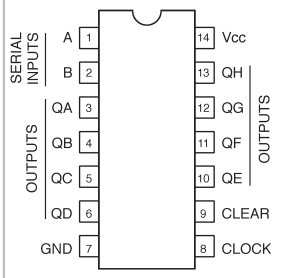
74 158 *quadruple sélecteur-multiplexeur 2 vers 1*



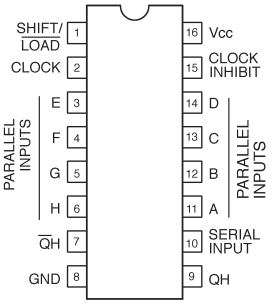
74 160/161/162/163 *compteur décimal synchrone*



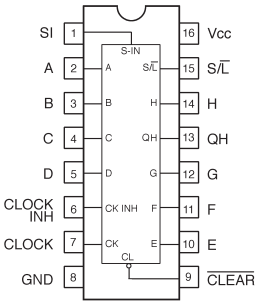
74 164 *registre à décalage 8 bits sortie parallèle*



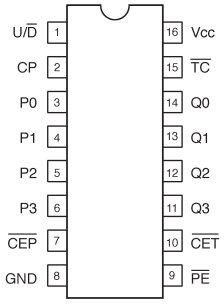
74 165 *registre à décalage 8 bits*



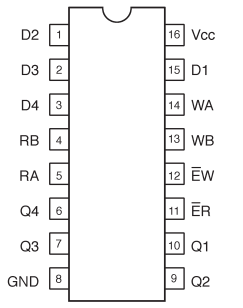
74 166 *registre à décalage 8 bits*



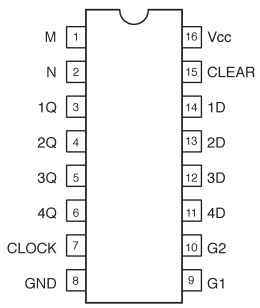
74 168/74 169 *compteur -décompteur (168 décimal, 169 binaire)*



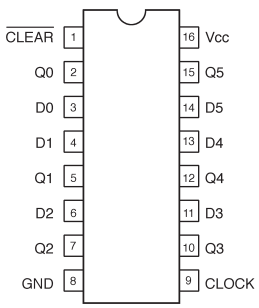
74 170 *registre 4 x 4 bits C.O.*



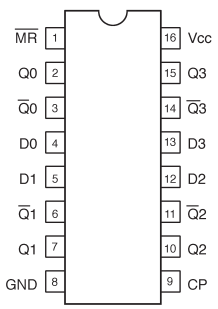
74 173 *registre D 4 bits sortie 3 états*



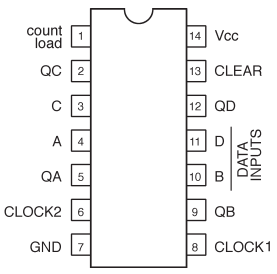
74 174 *sextuple bascule D avec R commune*



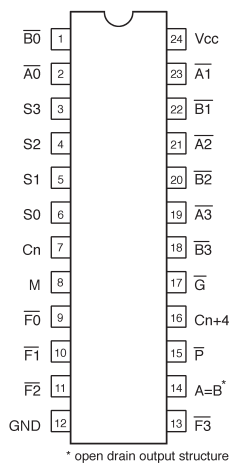
74 175 *quadruple bascule D avec R commune*



74 176 *compteur décimal programmable*

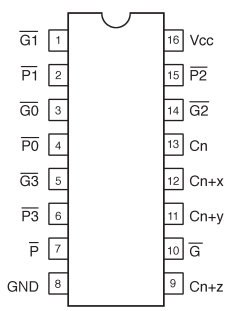


74 181 *ALU 4 bits et générateur de fonction*

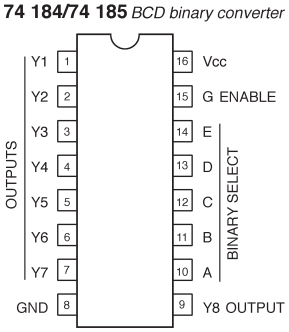


* open drain output structure

74 182 *générateur de report anticipé*

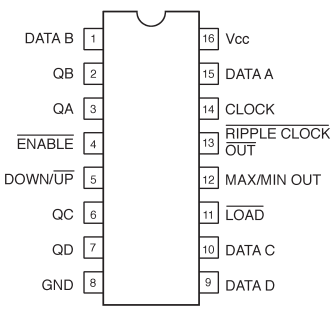


74 184/74 185 *BCD binary converter*



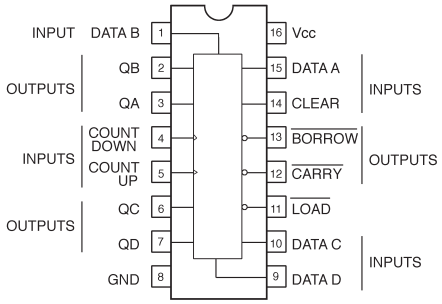
74 190/74 191

190 : compteur-décompteur BCD synchrone
191 : compteur-décompteur binaire synchrone



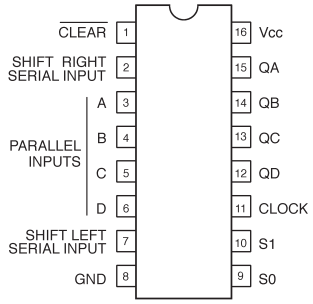
74 192/74 193

compteur-décompteur BCD synchrone avec 2 horloges et RAZ



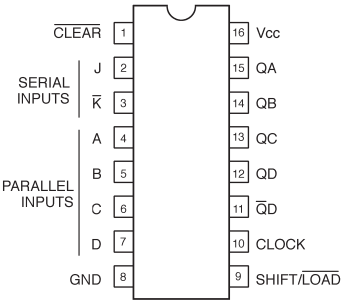
74 194

registre à décalage 4 bits bidirectionnel



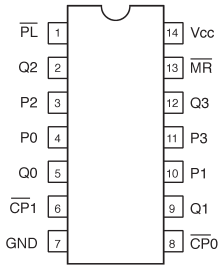
74 195

registre à décalage 4 bits



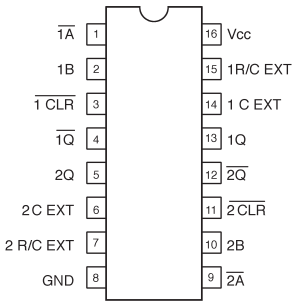
74 196/74 197

196 : compteur décimal programmable
197 : compteur binaire 4 bits programmable



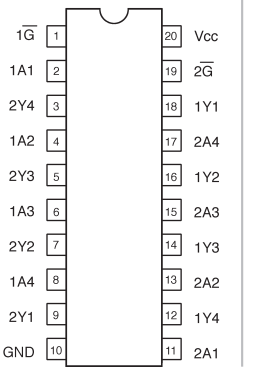
74 221

2 monostables avec entrées à bascule de Schmitt et RAZ



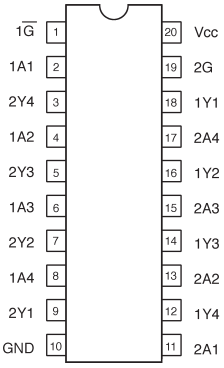
74 230

octal buffers and line drivers with 3 state outputs



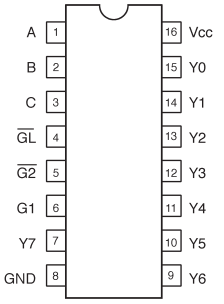
74 231

octal buffers and line drivers with 3 state outputs



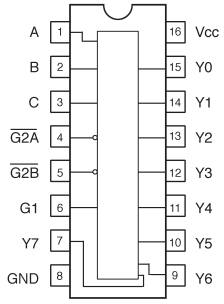
74 237

décodeur-démultiplexeur 3 vers 8 avec verrou d'adresse



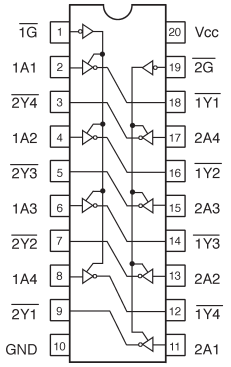
74 238

décodeur-démultiplexeur 3 vers 8



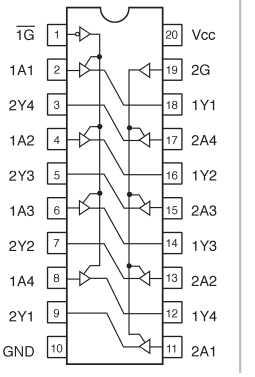
74 240

2 quadruples amplificateurs inverseurs de bus, 3 états



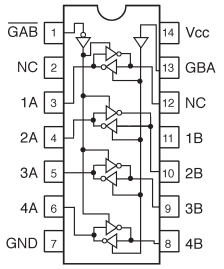
74 241

2 quadruples amplificateurs de bus, 3 états



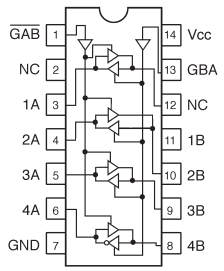
74 242

quadruple amplificateur-inverseur bidirectionnel, 3 états



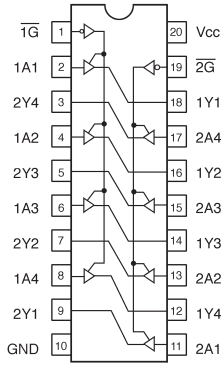
74 243

quadruple amplificateur bidirectionnel, 3 états



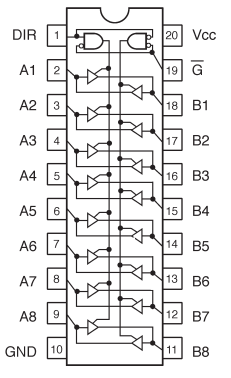
74 244

2 quadruples amplificateurs de bus 3 états



74 245

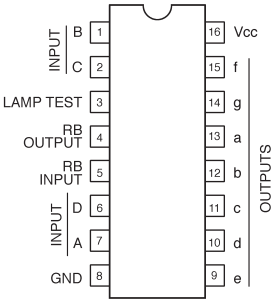
octuple amplificateur de bus bidirectionnel, 3 états



74 246/74 247
74 248/74 249

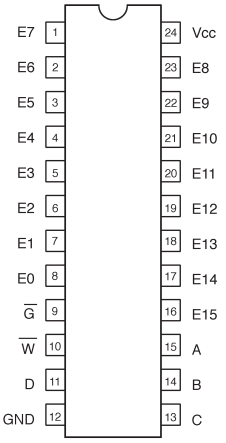
246 : C.O., 30 V
247 : C.O., 15 V
248 : 2 kΩ pull-up
249 : C.O., 5.5 V

décodeur BCD-7 segments de commande d'afficheur



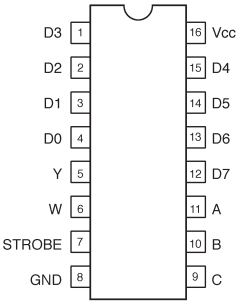
74 250

1 of 16 data generators/multiplexers with 3 state outputs

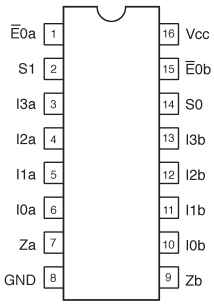


74 251

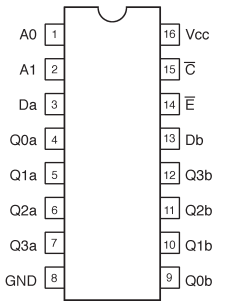
sélecteur-multiplexeur 8 vers 1 avec sorties 3 états



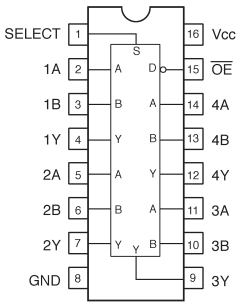
74 253 *double sélecteur-multiplexeur 4 vers 1 avec sorties 3 états*



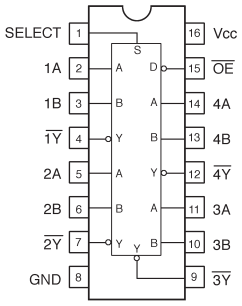
74 256 *dual 4 bit addressable latch*



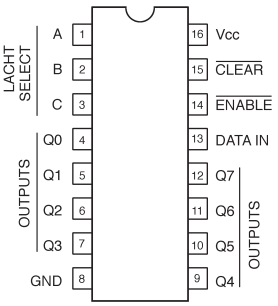
74 257 *quadruple sélecteur-multiplexeur 2 vers 1 avec sorties 3 états*



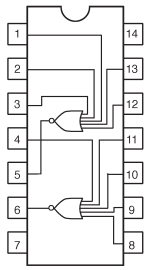
74 258 *quadruple sélecteur-multiplexeur 2 vers 1 avec sorties 3 états*



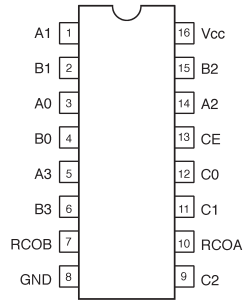
74 259 *octuple verrou adressable avec validation et RAZ*



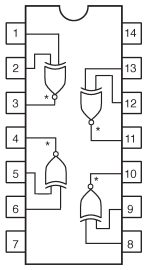
74 260 *dual 5 input nor gate*



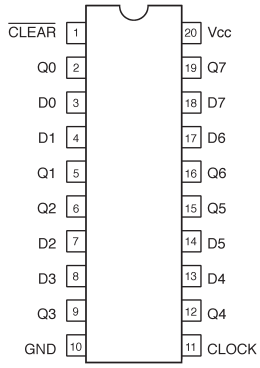
74 264 *look-ahead carry generators for counters*



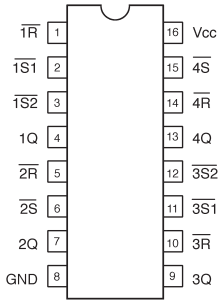
74 266 *4 portes OU-NON EXCLUSIF à 2 entrées C.O.*



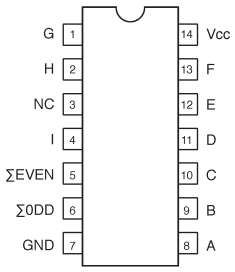
74 273 *octal D type flip-flops with clear*



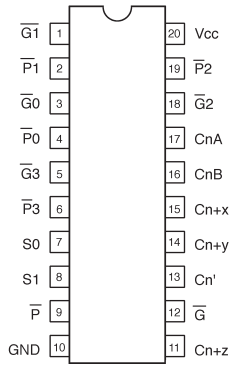
74 279 *quad set-reset latch*



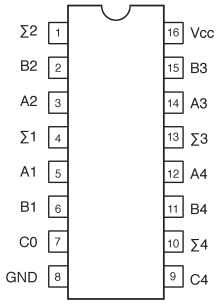
74 280 *générateur-contrôleur de parité 9 bits*



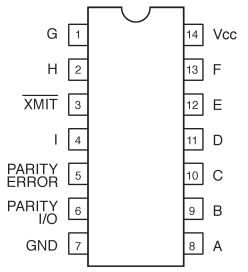
74 282 *look-ahead carry generator with selectable carry inputs*



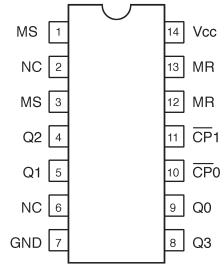
74 283 *additionneur complet 4 bits*



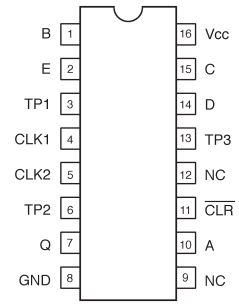
74 286 *9 bit parity generators/checker with bus driver parity I/O port*



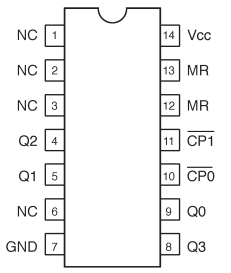
74 290 *compteur décimal*



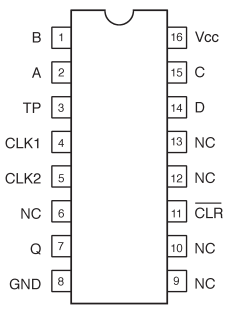
74 292 *diviseur de fréquence-temporisateur programmable*



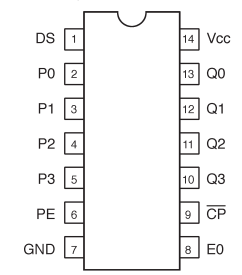
74 293 *compteur binaire asynchrone 4 bits*



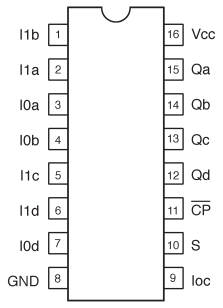
74 294 *diviseur de fréquence-temporisateur programmable*



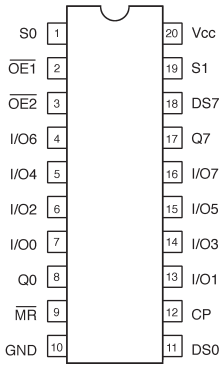
74 295 *registre à décalage 4 bits avec entrées et sorties en parallèle sortie 3 états*



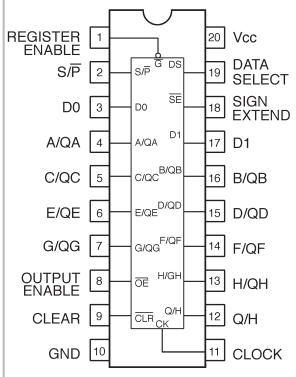
74 298 *quadruple sélecteur-multiplexeur 2 vers 1 avec mémoire*



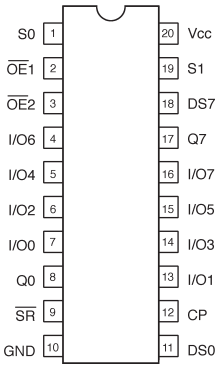
74 299 *registre à décalage universel 8 bits sortie 3 états*



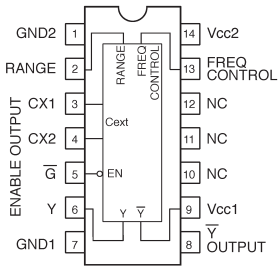
74 322 *8 bit shift registers with sign extend*



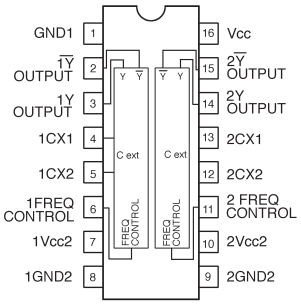
74 323 *registre à décalage universel 8 bits, sortie 3 états*



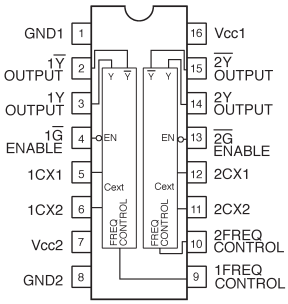
74 324 *voltage-controlled oscillators*



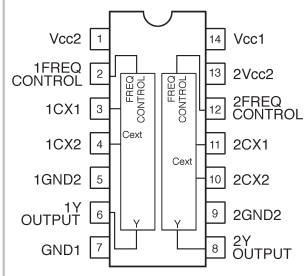
74 325 *voltage-controlled oscillators*



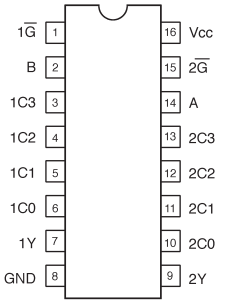
74 326 *voltage-controlled oscillators*



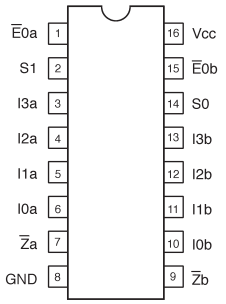
74 327 *voltage-controlled oscillators*



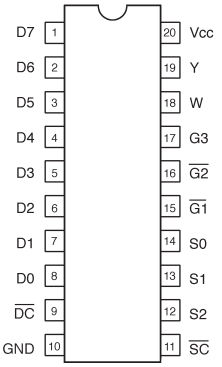
double sélecteur-multiplexeur 4 vers 1
74 352



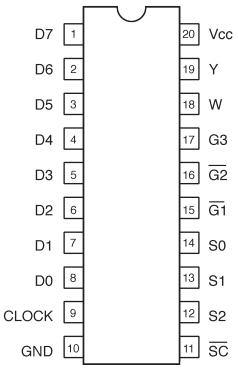
74 353
double sélecteur-multiplexeur 4 vers 1 avec sorties 3 états



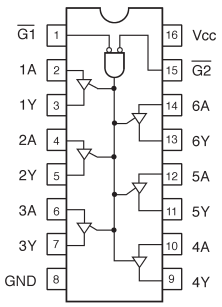
sélecteur-multiplexeur 8 vers 1
74 354 *avec registre d'entrée*



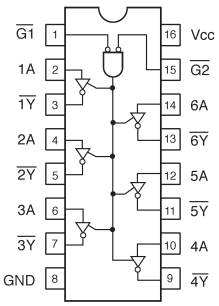
sélecteur-multiplexeur 8 vers 1
74 356 *avec registre d'entrée*



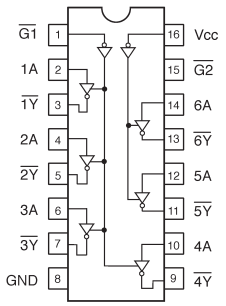
74 365
sextuple amplificateur, 3 états



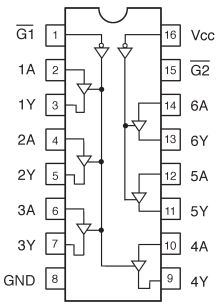
sextuple amplificateur inverseur 3 états
74 366



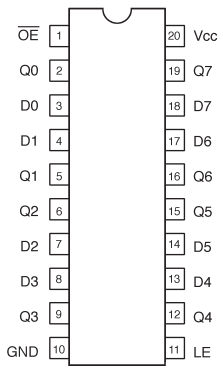
74 367 *double et quadruple amplificateurs, 3 états*



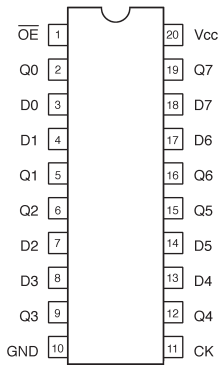
74 368 *double et quadruple amplificateurs inverseurs, 3 états*



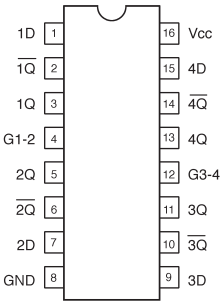
74 373 *octuple verrou, 3 états*



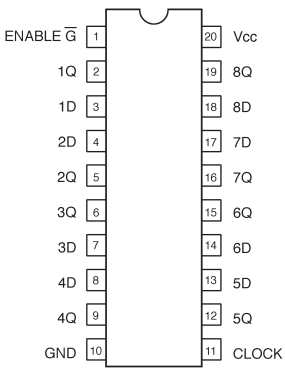
74 374
octuple bascule D sortie 3 états



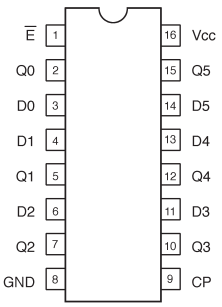
2 doubles bascules à verrouillage
74 375



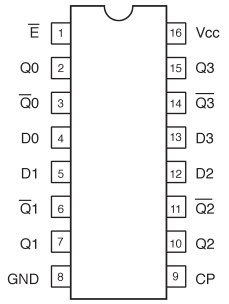
74 377 *octuple bascule D avec entrée de validation*



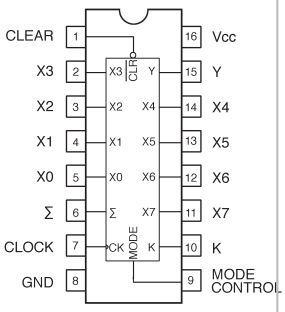
74 378 *sextuple bascule D avec entrée validation*



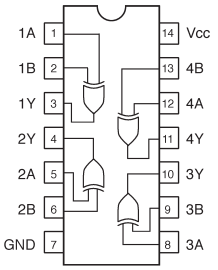
74 379 *quadruple bascule D avec entrée de validation*



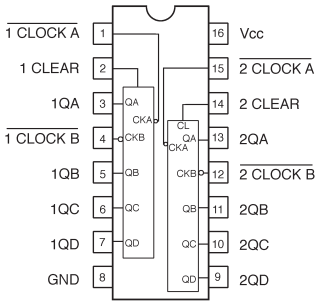
74 384 *8 bit by 1 bit two's-complement multiplier*



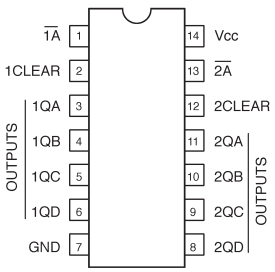
74 386 *4 portes OU-EXCLUSIF à 2 entrées*



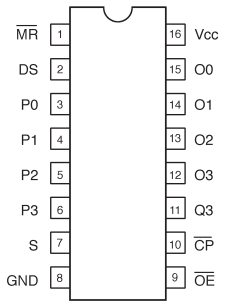
74 390 *2 compteurs décimaux*



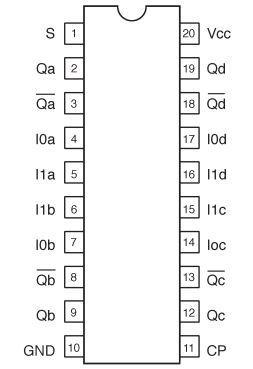
74 393 *2 compteurs binaires 4 bits*



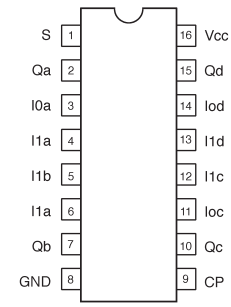
74 395 *registre à décalage 4 bits avec entrées et sorties en parallèle, sortie 3 états*



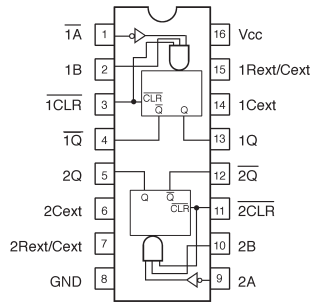
74 398 *quadruple sélecteur-multiplexeur 2 vers 1 avec mémoire*



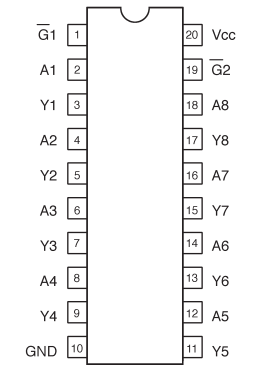
74 399 *quadruple sélecteur-multiplexeur 2 vers 1 avec mémoire*



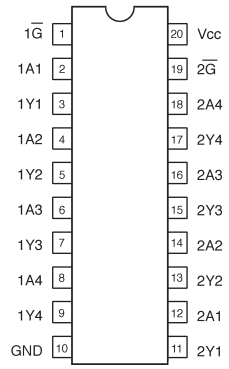
74 423 *2 monostables redéclenchables avec RAZ*



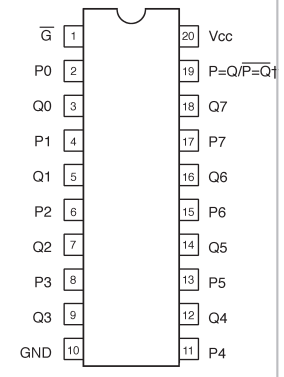
74 465/74 466 *octuple amplificateur 3 états*



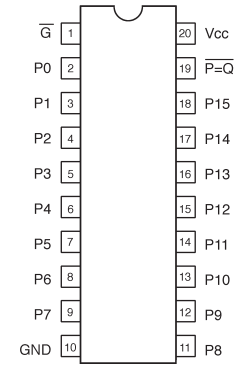
74 467/74 468 *2 quadruples amplificateurs de bus 3 états*



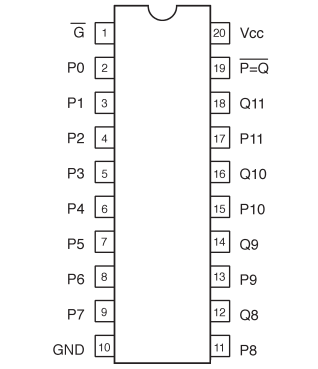
74 518/74 522 *8 bit identity comparators*



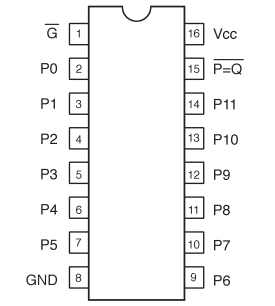
74 526 *fuse programmable identity comparators*



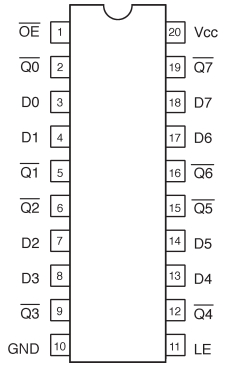
74 527 *fuse programmable identity comparators*



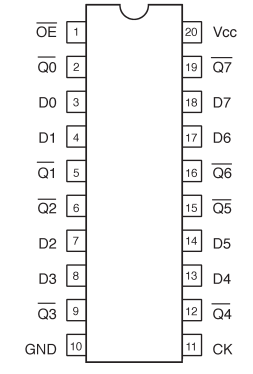
74 528 *fuse programmable identity comparators*



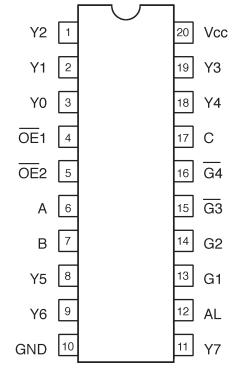
74 533 *octuple verrou inverseur 3 états*



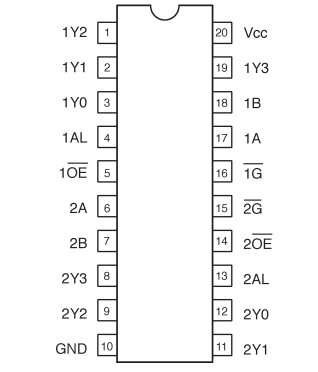
74 534 *octuple bascule D sortie inversée 3 états*



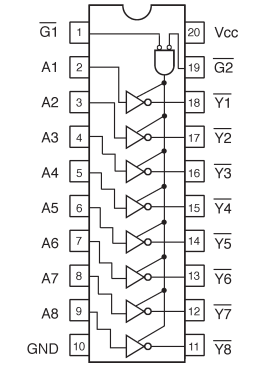
74 538 *3 line to 8 line decoders/demultiplexers with 3 state outputs*



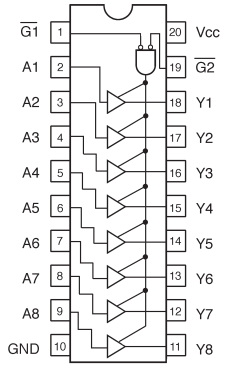
74 539 *dual 2 line to 4 line decoders/demultiplexers with 3 state outputs*



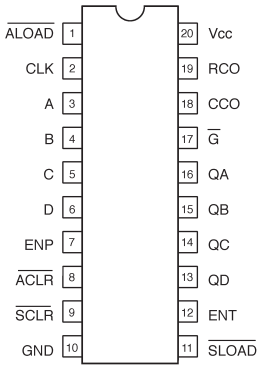
74 540 *octuples amplificateurs inverseurs de bus 3 états*



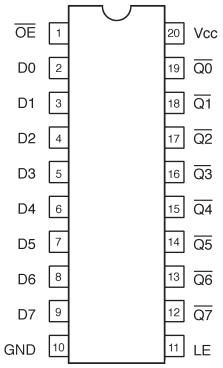
74 541 *octuple amplificateur 3 états*



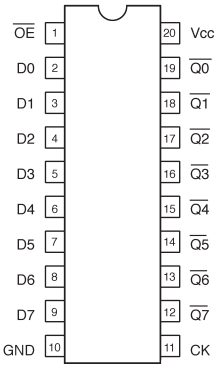
74 560/74 561 *synchronous 4 bit counters with 3 state outputs*



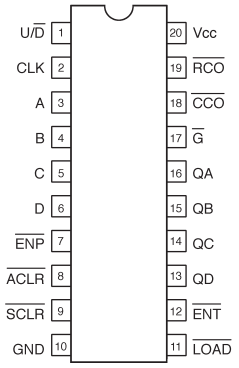
74 563 *octuple verrou inverseur 3 états*



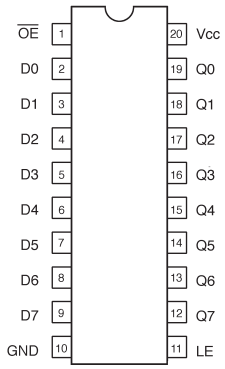
74 564 *octuple bascule D sortie inversée 3 états*



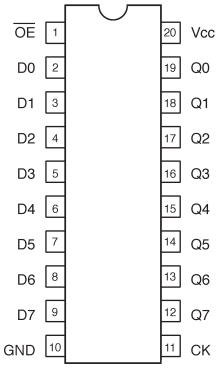
74 568/74 569 *synchronous 4 bit up/down decade and binary counters with 3 state outputs*



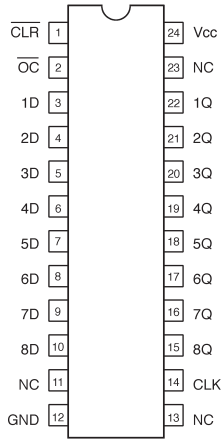
74 573 *octuple verrou 3 états*



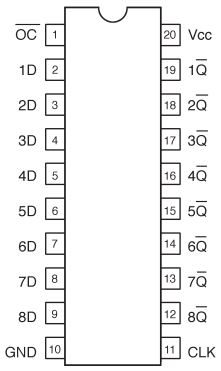
74 574 *octuple bascule D sortie 3 états*



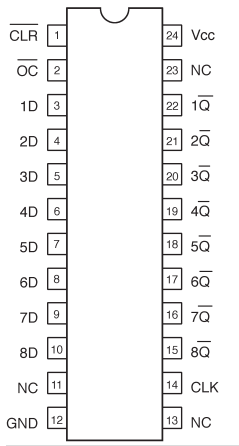
74 575 *octuple bascule D sortie 3 états*



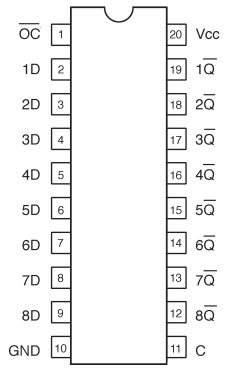
74 576 *octuple bascule D sortie 3 états*



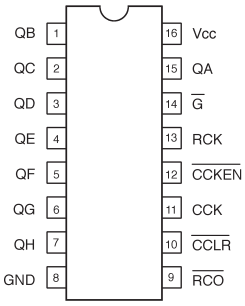
74 577 *octuple bascule D sortie 3 états*



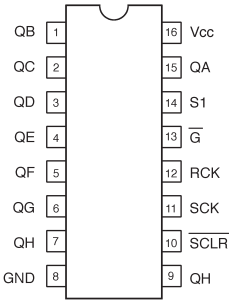
74 580 *octal D type transparent latches*



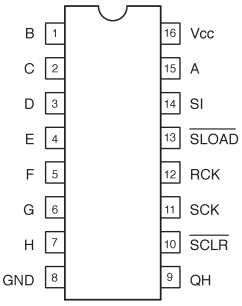
74 590 *compteur binaire 8 bits avec registre*



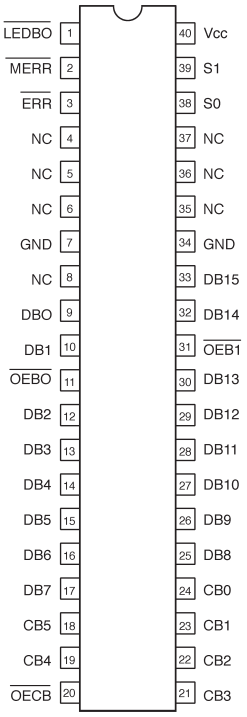
74 595 *registre à décalage 8 bits avec registre de sortie*



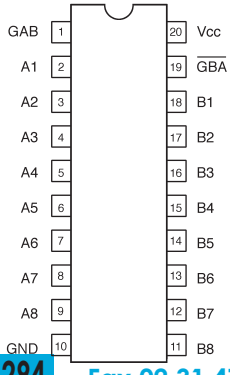
74 597 *registre à décalage 8 bits avec registre d'entrée*



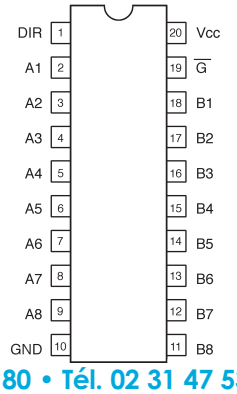
74 616/74 617 *16 bit parallel error detection and correction circuits*



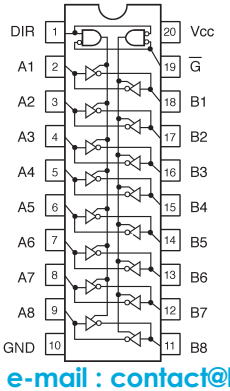
74 620/74 623 *octuple amplificateur de bus bidirectionnel, 3 états*



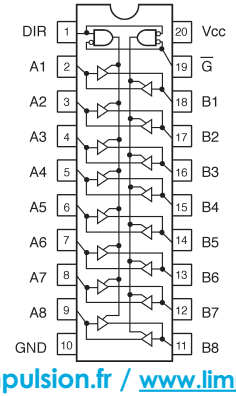
74 638/74 639 *octuple amplificateur-inverseur bidirectionnel, 3 états C.O.*



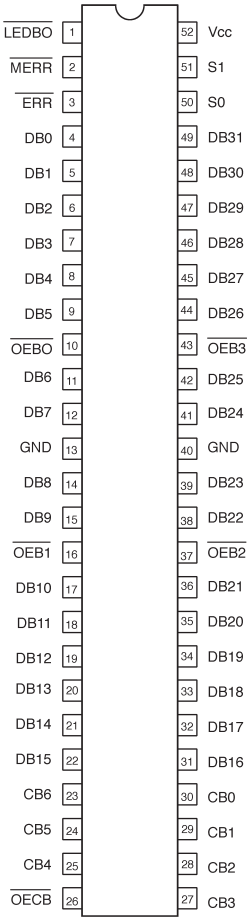
74 640 *octuple amplificateur-inverseur de bus bidirectionnel, 3 états*



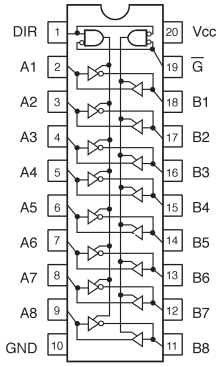
74 641 *octuple amplificateur de bus bidirectionnel C.O.*



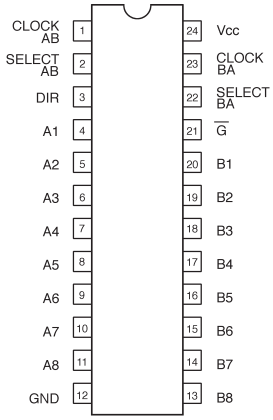
74 632/74 633
32 bit parallel error detection and correction circuits



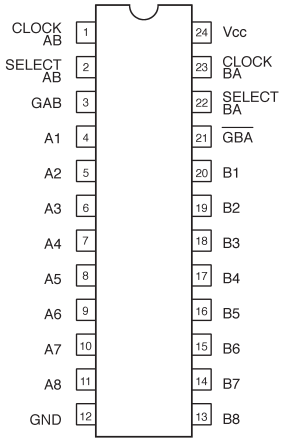
74 643
octuple amplificateur inverseur non inverseur, bidirectionnel



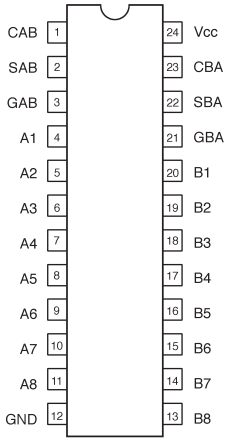
74 646...74 649
octuple amplificateur de bus bidirectionnel avec registre



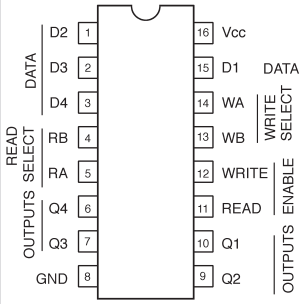
74 651/74 652
octuple amplificateur de bus bidirectionnel avec registre



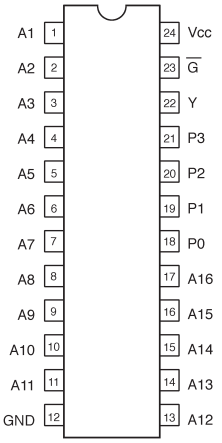
74 654
octuple amplificateur de bus bidirectionnel avec registre



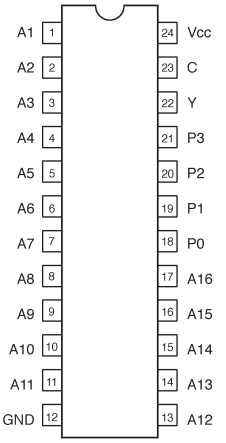
74 670 *registre 4 x 4 bits, sorties 3 états*



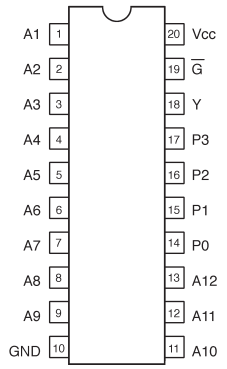
74 677
comparateur d'adresses 16 bits



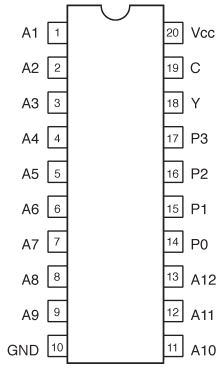
74 678
comparateur d'adresses 16 bits



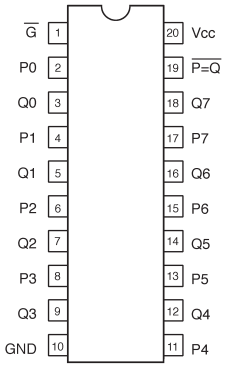
74 679
comparateur d'adresses 12 bits



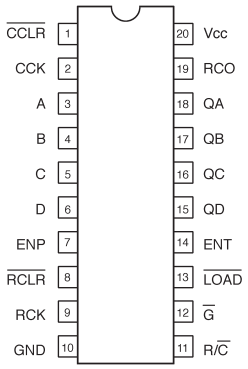
74 680
comparateur d'adresses 12 bits



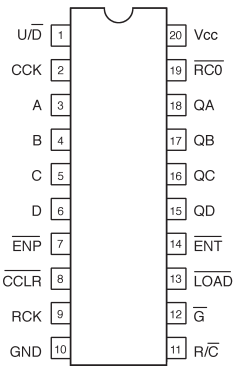
74 688/74 689
comparateur 8 bits



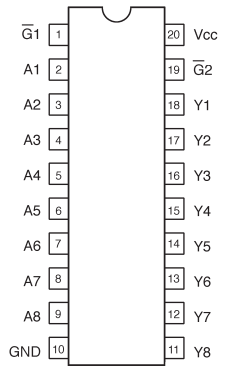
74 690...74 693
compteur synchrone 4 bits avec registre et multiplexeur



74 696/74 697
compteur-décompteur synchrone 4 bits avec registre et multiplexeur



74 746/74 747
octal buffers and line drivers with input pull-up resistors



CIRCUIT INTEGRE TTL — série 74 HC

CODE	DÉSIGNATION		1	10	25
74 HC 00	DIP 14	Quad 2-input NAND gate	-	-	-
74 HC 02	DIP 14	Quad 2-input NOR gate	-	-	-
74 HC 03	DIP 14	Quad 2-input NAND gate	-	-	-
74 HC 04	DIP 14	Hex inverter	-	-	-
74 HCU 04	DIP 14	Hex inverter	-	-	-
74 HC 05	DIP 14	Hex Inverter w-O/C Output	-	-	-
74 HC 08	DIP 14	Quad 2-input AND gate	-	-	-
74 HC 10	DIP 14	Triple 3-input NAND gate	-	-	-
74 HC 11	DIP 14	Triple 3-input AND gate	-	-	-
74 HC 14	DIP 14	Hex inverting Schmitt trigger	-	-	-
74 HC 20	DIP 14	Dual 4-input NAND gate	-	-	-
74 HC 21	DIP 14	Dual 4-input AND gate	-	-	-
74 HC 27	DIP 14	Triple 3-input NOR gate	-	-	-
74 HC 30	DIP 14	8-input NAND gate	-	-	-
74 HC 32	DIP 14	Quad 2-input NAND Schmitt trigger	-	-	-
74 HC 42	DIP 16	BCD to decimal decoder (1-of-10)	-	-	-
74 HC 51	DIP 14	Dual 2-Input AND-OR Inverter Gates	-	-	-
74 HC 58	DIP 14	Dual AND-OR gate	-	-	-
74 HC 73	DIP 14	Dual JK flip-flop with reset; negative-edge trigger	-	-	-
74 HC 74	DIP 14	Dual D-type flip-flop with set and reset; positive-edge trigger	-	-	-
74 HC 75	DIP 16	Quad bistable transparent latch	-	-	-
74 HC 76	DIP 16	Dual J-K flip flop + pset/clear	-	-	-
74 HC 85	DIP 16	4-bit magnitude comparator	-	-	-
74 HC 86	DIP 14	Quad 2-input EXCLUSIVE-OR gate	-	-	-
74 HC 107	DIP 14	Dual JK flip-flop with reset; negative-edge trigger	-	-	-
74 HC 109	DIP 16	Description : Dual JK flip-flop with set and reset; positive-edge trigger	-	-	-
74 HC 112	DIP 16	Dual JK flip-flop with set and reset; negative-edge trigger	-	-	-
74 HC 123	DIP 16	Dual retriggerable monostable multivibrator with reset	-	-	-
74 HC 125	DIP 14	Quad buffer/line driver; 3-state	-	-	-
74 HC 126	DIP 14	Quad buffer/line driver; 3-state	-	-	-
74 HC 132	DIP 14	Quad 2-input NAND Schmitt trigger	-	-	-
74 HC 137	DIP 16	3-to-8 line decoder/demultiplexer with address latches; inverting	-	-	-
74 HC 138	DIP 16	3-to-8 line decoder/demultiplexer; inverting	-	-	-
74 HC 139	DIP 16	Dual 2-to-4 line decoder/demultiplexer	-	-	-
74 HC 147	DIP 16	10-to-4 line priority encoder	-	-	-
74 HC 148	DIP 16	8-input priority encoder	-	-	-
74 HC 151	DIP 16	8-input multiplexer	-	-	-
74 HC 153	DIP 16	Dual 4-input multiplexer	-	-	-
74 HC 154	DIP 24	4-to-16 line decoder/demultiplexer (entraxe 7.62)	-	-	-
74 HC 154 P15	DIP 24	4-to-16 line decoder/demultiplexer (entraxe 15.24)	-	-	-
74 HC 157	DIP 16	Quad 2-input multiplexer	-	-	-
74 HC 158	DIP 16	Quad 2-input multiplexer; inverting	-	-	-
74 HC 160	DIP 16	Presettable synchronous BCD decade counter; asynchronous reset	-	-	-
74 HC 161	DIP 16	Presettable synchronous 4-bit binary counter; asynchronous reset	-	-	-
74 HC 162	DIP 16	Presettable synchronous BCD decade counter; synchronous reset	-	-	-
74 HC 163	DIP 16	Presettable synchronous 4-bit binary counter; synchronous reset	-	-	-
74 HC 164	DIP 14	8-bit serial-in/parallel-out shift register	-	-	-
74 HC 165	DIP 16	8-bit parallel-in/serial-out shift register	-	-	-
74 HC 166	DIP 16	8-bit parallel-in/serial-out shift register	-	-	-
74 HC 173	DIP 16	Quad D-type flip-flop; positive-edge trigger; 3-state	-	-	-
74 HC 174	DIP 16	Hex D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HC 175	DIP 16	Quad D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HC 190	DIP 16	Presettable Synchronous BCD Decade Up/Down Counter	-	-	-
74 HC 191	DIP 16	Presettable synchronous 4-bit binary up/down counter	-	-	-
74 HC 192	DIP 16	Decade Up/Down Counter with Clear	-	-	-
74 HC 193	DIP 16	Presettable synchronous 4-bit binary up/down counter	-	-	-
74 HC 194	DIP 16	4-bit bidirectional universal shift register	-	-	-
74 HC 195	DIP 16	4 bit parallel-access shift register	-	-	-

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CIRCUIT INTEGRE TTL — série 74 HC

CODE		DÉSIGNATION	1	10	25
74 HC 221	DIP 16	Dual non-retriggerable monostable multivibrator with reset	-	-	-
74 HC 237	DIP 16	3-to-8 line decoder/demultiplexer with address latches	-	-	-
74 HC 238	DIP 16	3-to-8 line decoder/demultiplexer	-	-	-
74 HC 240	DIP 20	Octal buffer/line driver; 3-state; inverting	-	-	-
74 HC 241	DIP 20	Octal buffer/line driver; 3-state	-	-	-
74 HC 242	DIP 14	Inverting Quad Tri-State Transceiver	-	-	-
74 HC 243	DIP 14	Quad bus transceiver; 3-state	-	-	-
74 HC 244	DIP 20	Octal buffer/line driver; 3-state	-	-	-
74 HC 245	DIP 20	Octal bus transceiver; 3-state	-	-	-
74 HC 251	DIP 16	8-input multiplexer; 3-state	-	-	-
74 HC 253	DIP 16	Dual 4-input multiplexer; 3-state	-	-	-
74 HC 257	DIP 16	Quad 2-input multiplexer; 3-state	-	-	-
74 HC 259	DIP 16	8-bit addressable latch	-	-	-
74 HC 273	DIP 20	Octal D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HC 280	DIP 14	9-bit odd/even parity generator/checker	-	-	-
74 HC 283	DIP 16	4-bit binary full adder with fast carry	-	-	-
74 HC 297	DIP 16	High Speed CMOS Logic Digital Phase-Locked-Loop	-	-	-
74 HC 299	DIP 20	8-bit universal shift register; 3-state	-	-	-
74 HC 365	DIP 16	Hex buffer/line driver; 3-state	-	-	-
74 HC 366	DIP 16	Hex buffer/line driver; 3-state; inverting	-	-	-
74 HC 367	DIP 16	Hex buffer/line driver; 3-state	-	-	-
74 HC 373	DIP 20	Octal D-type transparent latch; 3-state	-	-	-
74 HC 374	DIP 20	Octal D-type flip-flop; positive edge-trigger; 3-state	-	-	-
74 HC 377	DIP 20	Octal D-type flip-flop with data enable; positive-edge trigger	-	-	-
74 HC 390	DIP 16	Dual decade ripple counter	-	-	-
74 HC 393	DIP 14	Dual 4-bit binary ripple counter	-	-	-
74 HC 540	DIP 20	Octal buffer/line driver; 3-state; inverting	-	-	-
74 HC 541	DIP 20	Octal buffer/line driver; 3-state	-	-	-
74 HC 563	DIP 20	Octal D-type transparent latch; 3-state; inverting	-	-	-
74 HC 573	DIP 20	Octal D-type transparent latch; 3-state	-	-	-
74 HC 574	DIP 20	Octal D-type flip-flop; positive edge-trigger; 3-state	-	-	-
74 HC 590	DIP 16	8-bit binary counter w/ output register tri-state	-	-	-
74 HC 595	DIP 16	8-bit serial-in/serial or parallel-out shift register with output latches; 3-state	-	-	-
74 HC 640	DIP 20	Octal bus transceiver; 3-state; inverting	-	-	-
74 HC 643	DIP 20	Octal bus transceiver	-	-	-
74 HC 688	DIP 20	8-bit magnitude comparator	-	-	-
74 HC 4017	DIP 16	Johnson decade counter with 10 decoded outputs	-	-	-
74 HC 4020	DIP 16	14-stage binary ripple counter	-	-	-
74 HC 4024	DIP 14	7-stage binary ripple counter	-	-	-
74 HC 4040	DIP 16	12-stage binary ripple counter	-	-	-
74 HC 4046	DIP 16	Phase Lock Loop	-	-	-
74 HC 4049	DIP 16	Hex inverting high-to-low level shifter	-	-	-
74 HC 4050	DIP 16	Hex high-to-low level shifter	-	-	-
74 HC 4051	DIP 16	8-channel analog multiplexer/demultiplexer	-	-	-
74 HC 4052	DIP 16	Dual 4-channel analog multiplexer/demultiplexer	-	-	-
74 HC 4053	DIP 16	Triple 2-channel analog multiplexer/demultiplexer	-	-	-
74 HC 4060	DIP 16	14-stage binary ripple counter with oscillator	-	-	-
74 HC 4066	DIP 14	Quad bilateral switches	-	-	-
74 HC 4067	DIP 24	16-channel analog multiplexer/demultiplexer	-	-	-
74 HC 4075	DIP 14	Triple 3-input OR gate	-	-	-
74 HC 4078	DIP 14	8-In NOR Gate	-	-	-
74 HC 4094	DIP 16	8-stage shift-and-store bus register	-	-	-
74 HC 4316	DIP 16	Quad bilateral switches	-	-	-

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CIRCUIT INTEGRE TTL — série 74 HC

CODE		DÉSIGNATION	1	10	25
74 HC 4510	DIP 16	BCD up/down counter	-	-	-
74 HC 4511	DIP 16	BCD to 7-segment latch/decoder/driver	-	-	-
74 HC 4516	DIP 16	Binary up/down counter	-	-	-
74 HC 4520	DIP 16	Dual 4-bit synchronous binary counter	-	-	-
74 HC 4538	DIP 16	Dual retriggerable precision monostable multivibrator	-	-	-
74 HC 4543	DIP 16	BCD to 7-Seg LCD Latch Decoder Driver	-	-	-

CIRCUIT INTEGRE TTL — série 74 HCT

CODE		DÉSIGNATION	1	10	25
74 HCT 00	DIP 14	Quad 2-input NAND gate	-	-	-
74 HCT 02	DIP 14	Quad 2-input NOR gate	-	-	-
74 HCT 03	DIP 14	Quad 2-input NAND gate	-	-	-
74 HCT 04	DIP 14	Hex inverter	-	-	-
74 HCT 05	DIP 14	Hex Inverter (Open Drain)			
74 HCT 08	DIP 14	Quad 2-input AND gate	-	-	-
74 HCT 10	DIP 14	Triple 3-input NAND gate	-	-	-
74 HCT 11	DIP 14	Triple 3-input AND gate	-	-	-
74 HCT 14	DIP 14	Hex inverting Schmitt trigger	-	-	-
74 HCT 20	DIP 14	Dual 4-input NAND gate	-	-	-
74 HCT 21	DIP 14	Dual 4-input AND gate	-	-	-
74 HCT 27	DIP 14	Triple 3-input NOR gate	-	-	-
74 HCT 30	DIP 14	8-input NAND gate	-	-	-
74 HCT 32	DIP 14	Quad 2-input OR gate	-	-	-
74 HCT 42	DIP 16	BCD to decimal decoder (1-of-10)	-	-	-
74 HCT 73	DIP 14	Dual JK flip-flop with reset; negative-edge trigger	-	-	-
74 HCT 74	DIP 14	Dual D-type flip-flop with set and reset; positive-edge trigger	-	-	-
74 HCT 85	DIP 16	4-bit magnitude comparator	-	-	-
74 HCT 86	DIP 14	Quad 2-input EXCLUSIVE-OR gate	-	-	-
74 HCT 93	DIP 14	4-bit binary ripple counter	-	-	-
74 HCT 107	DIP 14	Dual JK flip-flop with reset; negative-edge trigger	-	-	-
74 HCT 123	DIP 16	Dual retriggerable monostable multivibrator with reset	-	-	-
74 HCT 125	DIP 14	Quad buffer/line driver; 3-state			
74 HCT 132	DIP 14	Quad 2-input NAND Schmitt trigger	-	-	-
74 HCT 137	DIP 16	3-to-8 line decoder/demultiplexer with address latches; inverting	-	-	-
74 HCT 138	DIP 16	3-to-8 line decoder/demultiplexer; inverting	-	-	-
74 HCT 139	DIP 16	Dual 2-to-4 line decoder/demultiplexer	-	-	-
74 HCT 153	DIP 16	Dual 4-input multiplexer	-	-	-
74 HCT 154	DIP 24	4-to-16 line decoder/demultiplexer	-	-	-
74 HCT 157	DIP 16	Quad 2-input multiplexer	-	-	-
74 HCT 161	DIP 16	Presettable synchronous 4-bit binary counter; asynchronous reset	-	-	-
74 HCT 162	DIP 16	Presettable synchronous BCD decade counter	-	-	-
74 HCT 163	DIP 16	Presettable synchronous 4-bit binary counter; synchronous reset	-	-	-
74 HCT 164	DIP 14	8-bit serial-in/parallel-out shift register	-	-	-
74 HCT 165	DIP 16	8-bit parallel-in/serial-out shift register	-	-	-
74 HCT 173	DIP 16	Quad D-type flip-flop; positive-edge trigger; 3-state	-	-	-
74 HCT 174	DIP 16	Hex D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HCT 175	DIP 16	Quad D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HCT 193	DIP 16	Presettable synchronous 4-bit binary up/down counter	-	-	-
74 HCT 221	DIP 16	Dual non-retriggerable monostable multivibrator with reset	-	-	-
74 HCT 238	DIP 16	3-to-8 line decoder/demultiplexer	-	-	-
74 HCT 240	DIP 20	Octal buffer/line driver; 3-state; inverting	-	-	-
74 HCT 241	DIP 20	Octal buffer/line driver; 3-state	-	-	-
74 HCT 245	DIP 20	Octal bus transceiver; 3-state	-	-	-

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CIRCUIT INTEGRE TTL — série 74 HCT

CODE		DÉSIGNATION	1	10	25
74 HCT 251	DIP 16	8-input multiplexer; 3-state	-	-	-
74 HCT 257	DIP 16	Quad 2-input multiplexer; 3-state	-	-	-
74 HCT 259	DIP 16	8-bit addressable latch	-	-	-
74 HCT 273	DIP 20	Octal D-type flip-flop with reset; positive-edge trigger	-	-	-
74 HCT 283	DIP 16	4-bit binary full adder with fast carry	-	-	-
74 HCT 299	DIP 20	8-bit universal shift register; 3-state	-	-	-
74 HCT 373	DIP 20	Octal D-type transparent latch; 3-state	-	-	-
74 HCT 374	DIP 20	Octal D-type flip-flop; positive edge-trigger; 3-state	-	-	-
74 HCT 390	DIP 16	Dual decade ripple counter	-	-	-
74 HCT 393	DIP 14	Dual 4-bit binary ripple counter	-	-	-
74 HCT 540	DIP 20	Octal buffer/line driver; 3-state; inverting	-	-	-
74 HCT 541	DIP 20	Octal buffer/line driver; 3-state	-	-	-
74 HCT 573	DIP 20	Octal D-type transparent latch; 3-state	-	-	-
74 HCT 574	DIP 20	Octal D-type flip-flop; positive edge-trigger; 3-state	-	-	-
74 HCT 640	DIP 20	Octal bus transceiver; 3-state; inverting	-	-	-
74 HCT 688	DIP 20	8-bit magnitude comparator	-	-	-
74 HCT 4024	DIP 14	7-stage binary ripple counter	-	-	-
74 HCT 4040	DIP 16	12-stage binary ripple counter	-	-	-
74 HCT 4046	DIP 16	Phase-locked-loop with VCO	-	-	-
74 HCT 4051	DIP 16	8-channel analog multiplexer/demultiplexer	-	-	-
74 HCT 4052	DIP 16	Dual 4-channel analog multiplexer, demultiplexer	-	-	-
74 HCT 4053	DIP 16	Triple 2-channel analog multiplexer/demultiplexer	-	-	-
74 HCT 4060	DIP 16	14-stage binary ripple counter with oscillator	-	-	-
74 HCT 4511	DIP 16	BCD to 7-segment latch/decoder/driver	-	-	-

CIRCUIT INTEGRE TTL — série SN 74 LS

Spécifications techniques

Alimentation : 5 V. Température d'utilisation : 0° C à + 70° C.
Temps de propagation 9 ns pour une consommation de 2 mW. Boîtier DIP.

CODE		DÉSIGNATION	1	10	25
74 LS 00	DIP 14	Quad 2-input NAND Gate	-	-	-
74 LS 01	DIP 14	Quad 2-Input NAND Gate, Open-Collector	-	-	-
74 LS 02	DIP 14	Quad 2-input NOR Gate	-	-	-
74 LS 03	DIP 14	Quad 2-Input NAND Gate, Open-Collector	-	-	-
74 LS 04	DIP 14	Hex Inverter	-	-	-
74 LS 05	DIP 14	Hex Inverter, Open-Collector	-	-	-
74 LS 08	DIP 14	Quad 2-Input AND Gate	-	-	-
74 LS 09	DIP 14	Quad 2-Input AND Gate, Open Collector	-	-	-
74 LS 10	DIP 14	Triple 3-Input NAND Gate	-	-	-
74 LS 11	DIP 14	Triple 3-Input AND Gate	-	-	-
74 LS 13	DIP 14	Dual 4-Input NAND Schmitt Trigger	-	-	-
74 LS 14	DIP 14	Hex Schmitt-Trigger Inverters	-	-	-
74 LS 15	DIP 14	Triple 3-Input AND Gate, Open-Collector	-	-	-
74 LS 20	DIP 14	Dual 4-input positive-NAND gates	-	-	-
74 LS 21	DIP 14	Dual 4-Input And Gate	-	-	-
74 LS 22	DIP 14	Dual 4-Input NAND Gate, Open Collector	-	-	-
74 LS 24		Integrated Circuit	-	-	-
74 LS 26	DIP 14	Quad 2-Input NAND Buffer, Open Collector	-	-	-
74 LS 27	DIP 14	Triple 3-Input NOR Gate	-	-	-
74 LS 28	DIP 14	Quad 2-Input NOR Buffer	-	-	-
74 LS 30	DIP 14	8-Input NAND Gate	-	-	-
74 LS 32	DIP 14	Quad 2-Input OR Gate	-	-	-
74 LS 33	DIP 14	Quad 2-Input NOR Buffer, Open Collector	-	-	-
74 LS 37	DIP 14	Quad 2-Input NAND Buffer	-	-	-
74 LS 38	DIP 14	Quad 2-Input NAND Buffer, Open Collector	-	-	-
74 LS 42	DIP 16	BCD to Decimal Decoder	-	-	-
74 LS 47	DIP 16	BCD to 7-Segment Decoder/Driver, Open Collector	-	-	-
74 LS 48	DIP 16	BCD to 7-Segment Decoder/Driver	-	-	-

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CIRCUIT INTEGRE TTL — série SN 74 LS

CODE	DÉSIGNATION		1	10	25
74 LS 51	DIP 14	2 Wide 2/3-Input AND/OR/INVERT Gate	-	-	-
74 LS 54	DIP 14	3-2-2-3-Input AND-OR-INVERT Gate	-	-	-
74 LS 55	DIP 14	2-Wide 4-INput AND-OR-INVERT Gate	-	-	-
74 LS 57	DIP 8	Frequency Dividers	-	-	-
74 LS 73	DIP 14	Dual J-K Flip-Flops with Clear	-	-	-
74 LS 74	DIP 14	Dual D Flip-Flop Positiv-Edge-Triggered Flip-Flops with Preset and Clear	-	-	-
74 LS 75	DIP 16	4-Bit D Latch with Q and NotQ	-	-	-
74 LS 76	DIP 16	Dual J-K Flip-Flops with Preset and Clear	-	-	-
74 LS 85	DIP 16	4-Bit Magnitude Comparator	-	-	-
74 LS 86	DIP 14	Quad 2-Input Exclusive-OR Gates	-	-	-
74 LS 90	DIP 14	Decade Counter	-	-	-
74 LS 91	DIP 14	8-Bit Shift Register	-	-	-
74 LS 92	DIP 14	Divide-by-12 Counter	-	-	-
74 LS 93	DIP 14	4-Bit Binary Counter	-	-	-
74 LS 95	DIP 14	4-Bit Parallel-Access Shift Register			
74 LS 96	DIP 16	5-Bit IN/OUT Shift Register	-	-	-
74 LS 107	DIP 14	Dual J-K Negative Edge-Triggered Flip-Flop with Clear	-	-	-
74 LS 109	DIP 16	Dual J-K Edge-Triggered Flip-Flop with Preset and Clear	-	-	-
74 LS 112	DIP 16	Dual J-K Edge-Triggered Flip-Flop w/Preset			
74 LS 113	DIP 14	DUAL J-K Negative Edge-Triggered Flip-Flop	-	-	-
74 LS 114	DIP 14	DUAL J-K Negative Edge-Triggered Flip-Flop	-	-	-
74 LS 122	DIP 14	Retriggerable Monostable Multivibrators	-	-	-
74 LS 123	DIP 16	Retriggerable Monostable Multivibrators	-	-	-
74 LS 125	DIP 14	Quad 3-State Buffer, Low Enable	-	-	-
74 LS 126	DIP 14	Quad 3-State Buffer, High Enable	-	-	-
74 LS 132	DIP 14	Quad 2-Input NAND Schmitt Trigger	-	-	-
74 LS 136	DIP 14	Quad Exclusive OR Gate, Open-Collector	-	-	-
74 LS 138	DIP 16	1-of-8 Decoder/Demultiplexer	-	-	-
74 LS 139	DIP 16	Dual 1-of-4 Decoder/Demultiplexer	-	-	-
74 LS 145	DIP 16	1-of-10 Decoder/Driver, Open Collector	-	-	-
74 LS 148	DIP 16	8-Input to 3-Line Priority Encoder	-	-	-
74 LS 151	DIP 16	Data Selectors/Multiplexers (8-Input Multiplexer)	-	-	-
74 LS 153	DIP 16	Dual 4-Line To 1-Line Selectors/Multiplexers	-	-	-
74 LS 155	DIP 16	Dual 2-Line To 4-Line Decoders/Demultiplexers	-	-	-
74 LS 156	DIP 16	Dual 2-Line To 4-Line Decoders/Demultiplexers, Open Collector	-	-	-
74 LS 157	DIP 16	Quad 2-Line To 1-Line Selectors/Multiplexers, Non-Inverting	-	-	-
74 LS 158	DIP 16	Quad 2-Line To 1-Line Selectors/Multiplexers, Inverting	-	-	-
74 LS 160	DIP 16	Synchronous Presettable BCD Decade Counter, Asynchronous Master Reset	-	-	-
74 LS 161	DIP 16	Synchronous Presettable 4-Bit Binary Counter, Asynchronous Master Reset	-	-	-
74 LS 162	DIP 16	Synchronous Presettable BCD Decade Counter	-	-	-
74 LS 163	DIP 16	Synchronous Presettable 4- Bit Binary Counter	-	-	-
74 LS 164	DIP 14	8-Bit Serial-in, Parallel-Out Shift Register	-	-	-
74 LS 165	DIP 16	8-Bit Parallel-To-Serial Shift Register	-	-	-
74 LS 166	DIP 16	8-Bit Shift Register	-	-	-
74 LS 168	DIP 16	Up/Down BCD Decade Counter	-	-	-
74 LS 169	DIP 16	Module 16 Binary Up/Down Counter	-	-	-
74 LS 170	DIP 16	4x4 Register File, Open-Collector	-	-	-
74 LS 173	DIP 16	4-Bit D-Type Register, 3_State Outputs	-	-	-
74 LS 174	DIP 16	Hex D-Type Flip-Flop, Master Reset	-	-	-
74 LS 175	DIP 16	Quad D-Type Flip-Flop with Clear	-	-	-
74 LS 181	DIP 24	4-Bit Arithmetic Logic Unit	-	-	-
74 LS 191	DIP 16	Synchronous Up/Down Binary Counters with Down/Up Mode Control	-	-	-
74 LS 193	DIP 16	Presettable Binary Up/Down Counter	-	-	-
74 LS 195	DIP 16	4-Bit Parallel Access Shift Register (9300 Type)	-	-	-
74 LS 196	DIP 14	Presettable Decade Counters/Latches	-	-	-
74 LS 197	DIP 14	Presettable 4-Bit Binary Counters/Latches	-	-	-
74 LS 221	DIP 16	Dual Monostable Multivibrator with Schnitt-Trigger Inputs	-	-	-

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CIRCUIT INTEGRE TTL — série SN 74 LS

CODE	DÉSIGNATION		1	10	25
74 LS 240	DIP 20	Octal Buffer/Line Driver/3-State, Inverting	-	-	-
74 LS 241	DIP 20	Octal Buffer/Line Driver/3-State, Non-Inverting	-	-	-
74 LS 243	DIP 14	Quad Bus Transeiver, Non-Inverting	-	-	-
74 LS 244	DIP 20	Octal Buffer/Line Driver/3-State, Non-Inverting	-	-	-
74 LS 245	DIP 14	Octal Bidirectional Transeiver/3-State, Non-Inverting	-	-	-
74 LS 247	DIP 16	BCD to 7-Segment Decoder/Driver, Open Collector	-	-	-
74 LS 251	DIP 16	8-Input Multiplexer, 3-State	-	-	-
74 LS 253	DIP 16	Dual 4-Input Multiplexer, 3-State	-	-	-
74 LS 257	DIP 16	Quad 2-Input Multiplexer, Non-Inverting 3-State	-	-	-
74 LS 258	DIP 16	Quad 2-Input Multiplexer, Inverting 3-State	-	-	-
74 LS 259	DIP 16	8-Bit Addressable Latch (9334)	-	-	-
74 LS 261	DIP 16	Programmable Binary Multiplexer	-	-	-
74 LS 266	DIP 14	Quad Exclusive NOR Gate, Open-Collector	-	-	-
74 LS 273	DIP 20	Octal D-Type Flip-Flop with Clear	-	-	-
74 LS 279	DIP 16	Quad Set-Reset Latches	-	-	-
74 LS 280	DIP 14	9-Bit Odd/Even Parity Generator/Checker	-	-	-
74 LS 283	DIP 16	4-Bit Full Adder (Rotated LS83A)	-	-	-
74 LS 290	DIP 14	Decade Counter	-	-	-
74 LS 293	DIP 14	4-Bit Binary Counter	-	-	-
74 LS 299	DIP 20	8-Bit Universal Shift/Storage Register with Comon Parallel I/O Pins, 30-State	-	-	-
74 LS 323	DIP 20	8-Input Shift/Storage Register with Synchronous Reset and Common I/O Pins, 3-State	-	-	-
74 LS 353	DIP 16	Dual 4-Input Multiplexer/3-State LS352	-	-	-
74 LS 365	DIP 16	Hex Buffer with Common Enable, Non-Inverting, 3-State	-	-	-
74 LS 366	DIP 16	Hex Inverting Bufferwith Common Enable, 3-State	-	-	-
74 LS 367	DIP 16	Hex Buffer, 2/4 Bit, Non-Inverting, 3-State	-	-	-
74 LS 373	DIP 20	Octal Transparent Latch/3-State	-	-	-
74 LS 374	DIP 20	Octal D-Type Flip-Flop/3-State	-	-	-
74 LS 375	DIP 16	4-Bit D-Type Latch with Q and NotQ	-	-	-
74 LS 377	DIP 20	Octal D-Type Flip-Flop with Enable	-	-	-
74 LS 378	DIP 16	Parallel Hex D-Type Register, with Enable	-	-	-
74 LS 379	DIP 16	Quad Parallel Register, with Enable	-	-	-
74 LS 390	DIP 16	Dual Decade Counter	-	-	-
74 LS 395	DIP 16	4-Bit Shift Register, 3-State			
74 LS 393	DIP 14	Dual 4-Bit Binary Counter	-	-	-
74 LS 465	DIP 20	Octal Buffers With 3-State Outputs	-	-	-
74 LS 490	DIP 16	Dual Decade Counter	-	-	-
74 LS 541	DIP 20	Octal 3-State Driver, Non-Inverting	-	-	-
74 LS 573	DIP 20	Octal D-Type Latch	-	-	-
74 LS 640	DIP 20	Octal Bus Transceiver Inverting with 3-State Outputs	-	-	-
74 LS 641	DIP 20	Octal Bus Transceiver, Open Collector	-	-	-
74 LS 645	DIP 20	Octal Bus Transceiver	-	-	-
74 LS 645 1	DIP 20	Octal Bus Transceiver/IOL=48 mA 3-State	-	-	-
74 LS 670	DIP 16	4x4 Register File, 3-State	-	-	-
74 LS 682	DIP 20	8-Bit Magnitude Comparator	-	-	-
74 LS 684	DIP 20	8-Bit Magnitude Comparator	-	-	-
74 LS 688	DIP 20	8-Bit Magnitude Comparator	-	-	-

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CIRCUIT INTEGRE TTL — série SN 74

CODE	DÉSIGNATION		1	10	25
74 01	DIP 14	Quad 2-input open-collector NAND gates	-	-	-
74 03	DIP 14	Quad 2-input open-collector NAND gates	-	-	-
74 04	DIP 14	Hex inverters	-	-	-
74 05	DIP 14	Hex inverters with open collector outputs	-	-	-
74 06	DIP 14	Hex inverter buffers / drivers with high-voltage outputs	-	-	-
74 07	DIP 14	Hex Buffers/Drivers with Open-Collector High-Voltage Outputs	-	-	-
74 10	DIP 14	Triple 3-Input NAND Gate	-	-	-
74 12	DIP 14	Triple 3-Input NAND Gate, Open Collector	-	-	-
74 16	DIP 14	Hex Inverter Buffers/Drivers with Open-Collector High-Voltage Outputs	-	-	-
74 17	DIP 14	Hex Buffers/Drivers with Open-Collector High-Voltage Outputs	-	-	-
74 20	DIP 14	Dual 4-Input Nand Gate	-	-	-
74 23	DIP 14	Dual 4-Input NOR Gates with Strobe	-	-	-
74 25	DIP 14	Dual 4-Input NOR Gates with Strobe	-	-	-
74 26	DIP 14	Quad 2-Input NAND Buffer, Open Collector	-	-	-
74 32	DIP 14	Quad 2-Input OR Gate	-	-	-
74 38	DIP 14	Quad 2-Input NAND Buffer, Open Collector	-	-	-
74 40	DIP 14	Dual 4-Input NAND Buffer	-	-	-
74 42	DIP 16	BCD Decimal Decoder	-	-	-
74 43	DIP 16	Ex 3 To Decimal Decoder	-	-	-
74 44	DIP 16	4 to 10 Line Decoder	-	-	-
74 45	DIP 16	BCD-To-Decimal Decoders/Drivers	-	-	-
74 46	DIP 16	BCD to 7-Segment Decoder/Driver	-	-	-
74 47	DIP 16	BCD to 7-Segment Decoder/Driver, Open Collector	-	-	-
74 54	DIP 14	3-2-2-3-Input AND-OR-INVERT Gate	-	-	-
74 60	DIP 14	Dual 4 Input Expander	-	-	-
74 74	DIP 14	Dual D Flip-Flop Positiv-Edge-Triggered Flip-Flops with Preset and Clear	-	-	-
74 75	DIP 16	4-Bit D Latch with Q and NotQ	-	-	-
74 81	DIP 14	16 Bit RAM	-	-	-
74 91	DIP 14	8-Bit Shift Register	-	-	-
74 92	DIP 14	Divide-by-12 Counter	-	-	-
74 97	DIP 16	Synchronous 6-Bit Binary Rate Multipliers	-	-	-
74 104	DIP 14	J-K M/S FLIP FLOP	-	-	-
74 107	DIP 14	Dual J-K Negative Edge-Triggered Flip-Flop with Clear	-	-	-
74 109	DIP 16	Dual J-K Edge-Triggered Flip-Flop with Preset and Clear	-	-	-
74 116	DIP 24	Dual 4-Bit Latches with Clear	-	-	-
74 118	DIP 16	4 RS-Flip FLop	-	-	-
74 120	DIP 16	Dual Pulse Synchronizers/Drivers	-	-	-
74 121	DIP 14	Monostable Multivibrator with Schmitt-Trigger inputs	-	-	-
74 128	DIP 14	Line Drivers	-	-	-
74 132	DIP 14	Quad 2-Input NAND Schmitt Trigger	-	-	-
74 142	DIP 16	BCD Control Latch Driver	-	-	-
74 144	DIP 24	4-Bit Control Latch Driver	-	-	-
74 150	DIP 24	Data Selectors/Multiplexers	-	-	-
74 151	DIP 16	Data Selectors/Multiplexers (8-Input Multiplexer)	-	-	-
74 153	DIP 16	Dual 4-Line To 1-Line Selectors/Multiplexers	-	-	-
74 154	DIP 24	4-Line To 16-Line Decoders/Demultiplexers	-	-	-
74 155	DIP 16	Dual 2-Line To 4-Line Decoders/Demultiplexers	-	-	-
74 157	DIP 16	Quad 2-Line To 1-Line Selectors/Multiplexers, Non-Inverting	-	-	-
74 160	DIP 16	Synchronous Presettable BCD Decade Counter, Asynchronous Master Reset	-	-	-
74 161	DIP 16	Synchronous Presettable 4-Bit Binary Counter, Asynchronous Master Reset	-	-	-
74 162	DIP 16	Synchronous Presettable BCD Decade Counter	-	-	-
74 165	DIP 16	8-Bit Parallel-To-Serial Shift Register	-	-	-
74 170	DIP 16	4x4 Register File, Open-Collector	-	-	-
74 172	DIP 24	16-Bit Multiple-Port Register File with 3-State Outputs	-	-	-
74 176	DIP 14	35-MHz Presettable Decade and Binary Counters/Latches	-	-	-
74 179	DIP 16	4-Bit Shift Register DC	-	-	-

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CIRCUIT INTEGRE TTL — série SN 74

CODE	DÉSIGNATION		1	10	25
74 180	DIP 14	9-Bit Odd/Even Paryty Generators/Checkers	-	-	-
74 182	DIP 16	Look Ahead Carry Generator	-	-	-
74 184	DIP 16	BCD to Binary Converter	-	-	-
74 191	DIP 16	Synchronous Up/Down Binary Counters with Down/Up Mode Control	-	-	-
74 193	DIP 16	Presettable Binary Up/Down Counter	-	-	-
74 196	DIP 14	Presettable Decade Counters/Latches	-	-	-
74 199	DIP 24	8-Bit Shift Register			
74 221	DIP 16	Dual Monostable Multivibrator with Schnitt-Trigger Inputs	-	-	-
74 247	DIP 8	BCD to 7-Segment Decoder/Driver, Open Collector	-	-	-
74 259	DIP 16	8-Bit Addressable Latch	-	-	-
74 279	DIP 16	Quad Set-Reset Latches	-	-	-
74 283	DIP 16	4-Bit Full Adder	-	-	-
74 285	DIP 16	4-Bit by 4-Bit Parallel Binary Multipliers	-	-	-
74 390	DIP 16	Dual Decade Counter	-	-	-
74 393	DIP 14	Dual 4-Bit Binary Counter	-	-	-

CIRCUIT INTEGRE TTL — série 74 ACT

CODE	DÉSIGNATION		1	10	25
74 AC 08	DIP 14	Quad 2-Input AND Gate	-	-	-
74 ACT 04	DIP 14	Hex Inverter	-	-	-
74 ACT 05	DIP 14	Hex inverter, open drain outputs	-	-	-
74 ACT 14	DIP 14	Hex Inverter Schmitt Trigger Input	-	-	-
74 ACT 74	DIP 14	Dual D-Type Positive Edge-Triggered Flip-Flop	-	-	-
74 ACT 245	DIP 20	Octal Bidirectional Transceiver with 3-STATE Inputs/ Outputs	-	-	-
74 ACT 323	DIP 20	8-Bit Universal Shift/Storage Register with Synchronous Reset and Common I/O Pins	-	-	-

CIRCUIT INTEGRE TTL — série DM 74 ALS

CODE	DÉSIGNATION		1	10	25
74 ALS 00	DIP 14	Quad 2-Input NAND Gates	-	-	-
74 ALS 04	DIP 14	Hex Inverters	-	-	-
74 ALS 08	DIP 14	Quad 2-Input AND Gate	-	-	-
74 ALS 10	DIP 14	Triple 3-Input NAND Gates	-	-	-
74 ALS 20	DIP 14	Dual 4-Input NAND Gates	-	-	-
74 ALS 21	DIP 14	Dual 4-Input AND Gates	-	-	-
74 ALS 27	DIP 14	Triple 3-Input NOR Gates	-	-	-
74 ALS 32	DIP 14	Quad 2-Input OR Gate	-	-	-
74 ALS 133	DIP 16	13-Input NAND Gate	-	-	-
74 ALS 138	DIP 16	3 to 8 Line Decoder/Demultiplexer	-	-	-
74 ALS 139	DIP 16	Dual 1-of-4 decoder/demultiplexer	-	-	-
74 ALS 257	DIP 16	3-STATE Quad 1-of-2 Line Data Selector/Multiplexer	-	-	-
74 ALS 373	DIP 20	Octal D-Type 3-STATE Transparent Latches	-	-	-
74 ALS 541	DIP 20	Octal Buffers and Line Drivers with 3-STATE Outputs	-	-	-
74 ALS 574	DIP 20	Octal D-Type Edge Triggered Flip-Flops with 3-STATE Outputs	-	-	-
74 ALS 576	DIP 20	Octal D-Type Edge-Triggered Flip-Flops with 3-STATE Outputs	-	-	-
74 ALS 641ADW	DIP 20	Octal Bus Transceivers With Open-Collector Outputs	-	-	-
74 ALS 645	DIP 20	Octal Bus Transceivers	-	-	-
74 ALS 688	DIP 20	8-Bit Identity Comparators	-	-	-

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CIRCUIT INTEGRE TTL — série DM 74 AS

CODE	DÉSIGNATION		1	10	25
74 AS 04	DIP 14	Hex Inverter	-	-	-
74 AS 373	DIP 20	Octal D-Type Transparent Latch with 3-STATE Outputs	-	-	-

CIRCUIT INTEGRE TTL — série 74 F

CODE	DÉSIGNATION		1	10	25
74 F 00	DIP 14	Quad 2-Input NAND Gate	-	-	-
74 F 02	DIP 14	Quad 2-Input NOR Gate	-	-	-
74 F 08	DIP 14	Quad 2-Input AND Gate	-	-	-
74 F 20	DIP 14	Dual 4-Input NAND Gate	-	-	-
74 F 74	DIP 14	Dual D-Type Positive Edge-Triggered Flip-Flop	-	-	-
74 F 125	DIP 14	3-state Quad Buffer	-	-	-
74 F 126	DIP 14	3-state Quad Buffer	-	-	-
74 F 161	DIP 16	Synchronous Presettable 4-Bit Binary Counter (Asynchronous Reset)	-	-	-
74 F 194	DIP 16	4-Bit Bidirectional Universal Shift Register	-	-	-
74 F 244	DIP 20	Octal Buffer/Line Driver with 3-STATE Outputs	-	-	-
74 F 245	DIP 20	Octal Bidirectional Transceiver with 3-STATE Inputs/Outputs	-	-	-
74 F 269	DIP 24	8-Bit Bidirectional Binary Counter	-	-	-
74 F 521	DIP 20	8-Bit Identity Comparator	-	-	-
74 F 534	DIP 20	Octal D-Type Flip-Flop with 3-STATE Outputs	-	-	-

CIRCUIT INTEGRE TTL — série 74 C

CODE	DÉSIGNATION		1	10	25
74 C 00	DIP 14	Quad 2-Input NAND Gate	-	-	-
74 C 02	DIP 14	Quad 2-Input NOR Gate	-	-	-
74 C 04	DIP 14	Hex Inverter	-	-	-
74 C 08	DIP 14	Quad 2-Input AND Gate	-	-	-
74 C 10	DIP 14	Triple 3-Input NAND Gates	-	-	-
74 C 20	DIP 14	Dual 4-Input NAND Gate	-	-	-
74 C 30	DIP 14	8-Input NAND Gate	-	-	-
74 C 42	DIP 16	BCD-to-Decimal Decoder	-	-	-
74 C 48	DIP 16	BCD-to-7 Segment Decoder	-	-	-
74 C 73	DIP 14	Dual J-K Flip-Flops with Clear and Preset	-	-	-
74 C 74	DIP 14	Dual D-Type Flip-Flop	-	-	-
74 C 76	DIP 16	Dual J-K Flip-Flops with Clear and Preset	-	-	-
74 C 83	DIP 16	4-Bit Binary Full Adder	-	-	-
74 C 85	DIP 16	4-Bit Magnitude Comparator	-	-	-
74 C 89	DIP 16	64-Bit 3-STATE Random Access Read/Write Memory	-	-	-
74 C 90	DIP 14	4-Bit Decade Counter	-	-	-
74 C 93	DIP 14	4-Bit Binary Counter	-	-	-
74 C 150	DIP 24	16 to 1 line decoder	-	-	-
74 C 154	DIP 24	4-Line to 16-Line Decoder/Demultiplexer	-	-	-
74 C 164	DIP 14	8-Bit Parallel-Out Serial Shift Register	-	-	-
74 C 192	DIP 16	Synchronous BCD Up/Down Dual Clock Counter	-	-	-
74 C 193	DIP 16	Synchronous Binary Up/Down Dual Clock Counter	-	-	-
74 C 221	DIP 16	Dual Monostable Multivibrator	-	-	-
74 C 240	DIP 20	Inverting Octal Buffer and Line Driver with 3-STATE Outputs	-	-	-

dans la limite des stocks disponibles

CIRCUIT INTEGRE TTL — série 74 C

CODE	DÉSIGNATION		1	10	25
74 C 902	DIP 14	Hex Non-Inverting TTL Buffer	-	-	-
74 C 906	DIP 14	Hex Open Drain N-Channel Buffers	-	-	-
74 C 908	DIP 8	Dual CMOS 30-Volt Relay Driver	-	-	-
74 C 909	DIP 14	Quad* comparator	-	-	-
74 C 914	DIP 14	Hex Schmitt Trigger with Extended Input Voltage	-	-	-
74 C 922	DIP 18	CMOS 16 Key Keyboard Encoder	-	-	-
74 C 923	DIP 20	CMOS 20 Key Keyboard Encoder	-	-	-
74 C 925	DIP 16	CMOS 4 Digit Counter	-	-	-
74 C 926	DIP 18	CMOS 4-Digit Counter With Carry	-	-	-
74 C 927	DIP 18	4-Digit Multiplexed Output	-	-	-
74 C 928	DIP 18	4 Digit Counter W/ Segment DRV	-	-	-

CIRCUIT INTEGRE TTL — série SN 74 S

CODE	DÉSIGNATION		1	10	25
74 S 37	DIP 14	Quad 2-input positive-NAND buffers	-	-	-
74 S 74	DIP 14	Dual D-Type Positive-Edge-Triggered Flip-Flops With Preset And Clear	-	-	-
74 S 86	DIP 14	Quad 2-input Exclusive-OR gates	-	-	-
74 S 124	DIP 16	Dual voltage-controlled oscillators	-	-	-
74 S 133	DIP 16	13-Input Positive-NAND Gates	-	-	-
74 S 134	DIP 16	12 Input NAND Gate 3 State	-	-	-
74 S 138	DIP 16	3-line to 8-line decoder / demultiplexer	-	-	-
74 S 140	DIP 14	Dual 4-input positive-NAND 50-Ohm line drivers	-	-	-
74 S 163	DIP 16	4-Bit Binary Counters	-	-	-
74 S 174	DIP 16	Hex D-Type Flip-Flops With Clear	-	-	-
74 S 189	DIP 16	16X4-Bit RAM	-	-	-
74 S 226	DIP 16	Circuit intégré	-	-	-
74 S 374	DIP 20	Octal D-Type Positive Edge Triggered Flip-Flops with 3-State Outputs	-	-	-

CIRCUIT INTEGRE TTL — série SN 75

CODE	DÉSIGNATION		1	10	25
75 107	DIP 14	Dual Line Receiver	-	-	-
75 112	DIP 14	Dual Line Driver	-	-	-
75 114	DIP 16	Dual Differential Line Driver	-	-	-
75 115	DIP 16	Dual Differential Line Receiver	-	-	-
75 121	DIP 16	Dual Line Drivers	-	-	-
75 122	DIP 16	Triple Line Receivers	-	-	-
75 140	DIP 8	Dual Line Receiver	-	-	-
75 150	DIP 8	Dual Line Driver	-	-	-
75 154	DIP 16	Quadruple Differential Line Receiver	-	-	-
75 160	DIP 20	Octal General-Purpose Interface Bus Transceiver	-	-	-
75 161	DIP 20	Octal General-Purpose Interface Bus Transceiver	-	-	-
75 162	DIP 22	Octal General-Purpose Interface Bus Transceiver	-	-	-
75 163	DIP 20	Circuit intégré	-	-	-
75 369	DIP 14	Interface Dual Mos Clock driver	-	-	-
75 451	DIP 8	Dual Very-High Speed, High-Current Peripheral Drivers /AND	-	-	-
75 453	DIP 8	Dual Very-High Speed, High-Current Peripheral Drivers /OR	-	-	-
75 454	DIP 8	Dual Very-High Speed, High-Current Peripheral Drivers /NOR	-	-	-
75 462P	DIP 8	Dual High-Voltage, High-Current Peripheral Drivers	-	-	-
75 469	DIP 16	High-Voltage, High-Current Darlington Transistor Arrays	-	-	-
75 471	DIP 8	Dual High-Voltage, High-Current Peripheral Drivers /AND	-	-	-
75 472	DIP 8	Dual High-Voltage, High-Current Peripheral Drivers /NAND	-	-	-

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